- Show that the ABD algorithm executed by one writer and multiple readers implements a regular but not atomic register
- Turn the algorithm into an atomic 1WNR one
- An atomic NWNR?

 For a fault-free system, design a readoptimized quorum system:

 $\checkmark A$ read operation involves a single replica

- For a t-resilient system, design a quorum system ensuring a stronger property
 - ✓ $\forall W \in W_P, \forall R \in R_P$: $W \cap R$ contains at least one correct process

In a read-write shared memory model:

- Show that Lattice Agreement (LA) is equivalent to one-shot atomic snapshot (1AS)
 - ✓ Find the matching lattice and propose two-way wait-free transformations
 - $1AS \Leftrightarrow LA$

In a read-write shared memory model:

- Show that Generalized Lattice Agreement (GLA) is equivalent to (long-lived) atomic snapshot (AS)
 - ✓ Find the matching lattice and propose two-way wait-free transformations
 - AS \Leftrightarrow GLA