



# SEQUANS

World leader in LTE for IoT Chip Solutions

## 5G System Architect

*Sequans Communications S.A.* (NYSE: SQNS) is a leading provider of single-mode 4G LTE semiconductor solutions for the Internet of Things (IoT) and a wide range of broadband data devices.

Founded in 2003, Sequans has developed and delivered seven generations of 4G technology and its chips are certified and shipping in 4G networks around the world. Today, Sequans offers two LTE product lines: StreamrichLTE™, optimized for broadband devices, including CPE, mobile and portable routers, and high-performance IoT devices; and StreamliteLTE™, optimized for lower data rate and narrowband IoT devices, including wearables, trackers, and sensors. Sequans is based in Paris, France with additional offices in Sophia Antipolis, France; and in the United States, United Kingdom, Israel, Hong Kong, Singapore, Sweden, Taiwan, South Korea, and China.

At Sequans, we know that our success depends on our people, so we are always on the lookout for innovators, leaders, and visionaries. Our team is driven by a shared desire to lead in the IoT/5G industry.

We are looking for highly motivated team players with ideally records of achievement in wireless technologies to help us maintain and develop our leadership in the IoT/5G industry. We are searching a 5G system architect, to support the development of our 5G modem.

### RESPONSABILITIES

- You will be in charge of modem architecture, providing specifications, analysis and main design choices, thanks to interaction with the teams of algorithm development, HW design, DSP/embedded SW design and platform integration.
- You should contribute to the definition and application of a mature process to guarantee the delivery of solutions as per global Sequans quality policy throughout the various teams.
- You will be responsible to ensure that the technical definition of the modem will meet the product definition as set by strategy and marketing teams.

### PROFILE

- At least 10 years of experience in similar position
- Track record of successful design of ASIC and SoC
- Good technical knowledge in DSP/HW/ASIC design
- Strong expertise in chip architecture and dimensioning, HW/SW partitioning, tradeoff management, low power design
- Good knowledge of semiconductor market and processes
- Strong experience in working with various team, including cross cultural and off-shore teams
- Efficiency, commitment, leadership and charisma, customer driven, autonomy and ability to interact at all levels within the company
- Experience in wireless systems, ideally in 4G/5G

Please send your resume to [sophie@sequans.com](mailto:sophie@sequans.com) and [cdeazevedo@sequans.com](mailto:cdeazevedo@sequans.com). We thank all candidates for their interest in Sequans and will contact those who for whom we have a potential role.



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