optical spectrum analyzer, respectively. The operating process is the same as the experiment which has been described earlier in Section 4. The theoretical and experimental macrobending loss curves versus wavelength ranging from 1500 to 1600 nm for bend radius of 6.5 and 6 mm are presented in Figures 6(a) and 6(b), respectively, from which one can see that the theoretical bend loss agrees with the experimental results. As a comparison, the measured bend losses of bare SMF28 in Figure 5 are also presented. The difference of bend loss between the two cases, i.e., bare SMF28 and the bare SMF28 with an absorbing layer, shows that the reflection occurring at the interface between the cladding layer and air has a significant effect on the bend loss.

6. CONCLUSION

In conclusion, we have presented a thorough theoretical and experimental investigation of the macrobending loss characteristics of a standard single mode fiber with small bend radii, which includes theoretical modeling analysis for fiber bend loss, for SMF28 with coating layers and the bare SMF28 after stripping the coating layers and chemical etching of partial cladding. Both experimental and theoretical results have demonstrated the impact of reflection occurring at the interface between the cladding and coating layer or the cladding layer and air on the bend loss.

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ON THE DESIGN OF MULTIFREQUENCY DIVIDERS SUITABLE FOR GSM/DCS/PCS/UMTS APPLICATIONS BY USING A PARTICLE SWARM OPTIMIZATION-BASED TECHNIQUE

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ABSTRACT: A particle swarm optimization-based technique is applied to design dividers that operate in two or more frequency bands at once. The geometry of the dividers is optimized under specific requirements concerning the impedance-matching bandwidth and the complex current distribution on unmatched real or complex terminal loads for each one of the resonant frequencies. The required current distribution on the loads concerns not only the ratio between the current amplitudes but also the phase difference between the currents. Several cases are studied to show the robustness of the particle swarm optimizer as well as the ability of the technique to derive optimal multifrequency structures suitable for GSM/DCS/PCS/UMTS applications. © 2007 Wiley Periodicals, Inc. Microwave Opt Technol Lett 49: 2138–2144, 2007; Published online in Wiley InterScience (www.interscience.wiley.com). DOI 10.1002/mop.22658

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1. INTRODUCTION

Dividers are structures of great interest, especially for feeding networks of wireless or mobile communications systems [1–3]. The two main purposes of a divider are splitting the signal according to the desired split ratio and providing impedance matching inside the frequency range of operation [4]. Of course, a well-designed divider must satisfy an additional requirement concerning the desired phase difference between the signals applied on the terminal loads. So far, many methods have been proposed for the designing of dividers that satisfy the above requirements [5–29]. Most of these methods consider dividers operating in a single frequency band. However, the need for simultaneous operation in two or more frequency bands led to the designing of dual-frequency [28, 29] or multifrequency dividers.

A multifrequency divider must satisfy the earlier-mentioned requirements concerning the desired impedance-matching bandwidth and the desired signal-split ratio, including the phase difference between the signals applied on the terminal loads. The main difficulty in designing such a divider results from the fact that all the above requirements must be satisfied simultaneously in all the frequency bands, considering that the terminal loads are not matched to the main transmission line that feeds the divider. In addition, a multifrequency divider that complies with the above requirements must be easily implemented in practice.

To overcome the above difficulties, the present work introduces a new technique, which makes use of a particle swarm optimization-(PSO) based algorithm developed by the authors. The fundamentals of PSO have been discussed in many papers [30–39] and many problems have already been solved by applying PSO-based methods

[40–58]. According to our technique, the geometry of the divider is analyzed by using transmission line theory [59–62]. The results derived from the analysis are the impedance-matching bandwidth, the ratio between the amplitudes of the currents applied on the terminal loads, and the phase difference between these currents. The values of the above parameters are used for the estimation of a suitably defined mathematical expression called "fitness function." The basic idea implemented by the PSO algorithm is to maximize the fitness function. The fitness function achieves its global maximum when the above parameters reach their desired values. The geometry of the divider, that produces the fitness global maximum, is the optimum solution to the specific designing problem.

The proposed technique has been applied to optimize dividers for GSM/DCS operation and also for GSM/DCS/PCS/UMTS operation. Several cases were studied in this work with different values of current-split ratio at the terminal points of the divider and with different real or complex values of terminal loads. The results show that the technique is very effective and very useful in practice, because the resulting structures are realistic and can be fabricated by using microstrip technology.

2. FORMULATION

The proposed structure of the divider consists of two branches that feed two corresponding terminal loads, Z_{AT} and Z_{BT} , as shown in Figure 1. The loads may be real or complex and are generally not



matched to the main line that feeds the divider. Two types of parameters define the geometry of the divider: lengths and the characteristic impedances of the transmission lines used for the construction of the branches. The optimization of a structure under many constraints demands a geometry defined by many parameters. Every parameter can be considered as a degree of freedom available to optimize the divider. Therefore, each branch is assumed to be composed of tandem transmission line sections of different length and of different characteristic impedance. After several trials, it was found that four sections per branch are sufficient to optimize the divider. In particular, the branch A consists of four sections with lengths $D_{a1}, D_{a2}, D_{a3}, D_{a4}$ and corresponding characteristic impedances Z_{0a1}, Z_{0a2}, Z_{0a3}, Z_{0a4}. Similarly, the branch B consists of four sections with lengths D_{b1} , D_{b2} , D_{b3} , D_{b4} and corresponding characteristic impedances Z_{ob1} , Z_{ob2} , Zob3, Zob4. In total, there are 16 optimization parameters available to optimize the divider. Because of the stochastic nature of the optimization procedure, each one of the above parameters may take any real value. Therefore, it is desirable to fabricate the divider in microstrip form. The microstrip technology provides the advantage of fabricating the sections of the divider with any value of length and any value of characteristic impedance.

As mentioned earlier, a well-designed divider has to achieve the desired impedance-matching bandwidth (BW), meaning that the input impedance of the divider has to be as close as possible to the characteristic impedance of the main feeding line inside the frequency range of operation [4]. Actually, the impedance-matching bandwidth is the frequency range where the return loss (RL) is below -9.54 dB (approximately -10 dB). A way of calculating the RL is given below. The value of the terminal load Z_{AT} coincides with the input impedance at the position A_0 :

$$Z_{\rm A0} = Z_{\rm AT} \tag{1}$$

The input impedance at the position A_i (i = 0, ..., 3) is considered to be the terminal load of the section a_{i+1} . Using transmission line theory [59–62], the input impedances at the positions A_i (i = 1, ..., 4) are calculated recursively by the following expression:

$$Z_{\rm Ai} = Z_{\rm oal} \frac{Z_{\rm A(i-1)} + j Z_{\rm oai} \tan(\beta D_{\rm ai})}{Z_{\rm oai} + j Z_{\rm A(i-1)} \tan(\beta D_{\rm ai})}$$
(2)

where β is the phase constant inside the microstrip structure of the divider and $Z_{A(i-1)}$ is the input impedance at the position A_{i-1} . Similarly, the value of the terminal load Z_{BT} coincides with the input impedance at the position B_0 :

$$Z_{B0} = Z_{BT} \tag{3}$$

The input impedance at the position B_i (i = 0, ..., 3) is considered to be the terminal load of the section b_{i+1} . Thus, the input impedances at the positions B_i (i = 1, ..., 4) are calculated recursively by the following expression:

$$Z_{\mathrm{B}i} = Z_{\mathrm{ob}i} \frac{Z_{\mathrm{B}(i-1)} + j Z_{\mathrm{ob}i} \mathrm{tan}(\beta D_{\mathrm{b}i})}{Z_{\mathrm{ob}i} + j Z_{\mathrm{B}(i-1)} \mathrm{tan}(\beta D_{\mathrm{b}i})}$$
(4)

where $Z_{B(i-1)}$ is the input impedance at the position B_{i-1} . The position A_4 is identical with B_4 . Therefore, the input impedance Z_{in} of the divider is the parallel combination of Z_{A4} and Z_{B4} :

$$Z_{\rm in} = (Z_{\rm A4}^{-1} + Z_{\rm B4}^{-1})^{-1}$$
(5)

Figure 1 The proposed structure of the multifrequency divider

The main line that feeds the divider is considered to have a characteristic impedance of 50 Ω . Then, the RL at the input of the divider is calculated in decibels as follows:

$$RL = 20\log|(Z_{in} - 50)/(Z_{in} + 50)|[dB]$$
(6)

Another important factor is the current-split ratio (CR), which is defined as the ratio between the amplitudes of the currents applied respectively on the two loads. A way of calculating the CR is given below. Without loss of generality, all the quantities are considered normalized with respect to the average input power $P_{\rm in}$ of the divider, and therefore $P_{\rm in} = 1$ W. Given the value of $Z_{\rm in}$, $P_{\rm in}$ can be expressed in terms of the complex input voltage $V_{\rm in}$ of the divider as follows:

$$P_{\rm in} = 0.5 |V_{\rm in}|^2 \text{Real}[1/Z_{\rm in}^*]$$
(7)

where V_{in} is the amplitude of V_{in} and Z_{in}^* is the complex conjugate value of Z_{in} . Given that $P_{in} = 1$ W, the earlier equation yields:

$$V_{\rm in} = \sqrt{2/\text{Real}[1/Z_{\rm in}^*]} e^{j0}$$
(8)

where θ is the phase of V_{in} . Regarding V_{in} as reference voltage, we may assume that $\theta = 0$. Thus, Eq. (8) is simplified as follows:

$$V_{\rm in} = \sqrt{2/\text{Real}[1/Z_{\rm in}^*]} \tag{9}$$

 $V_{\rm in}$ coincides with the voltages at the positions A_4 and B_4 :

$$V_{\rm A4} = V_{\rm B4} = V_{\rm in} = \sqrt{2/\text{Real}[1/Z_{\rm in}^*]}$$
 (10)

Using transmission line theory [59–62], the voltages at the positions A_i (i = 3, ..., 0) are calculated recursively by the following expression:

$$V_{\rm Ai} = V_{\rm A(i+1)} \left[\cos(\beta D_{\rm a(i+1)}) - j \frac{Z_{\rm oa(i+1)}}{Z_{\rm A(i+1)}} \sin(\beta D_{\rm a(i+1)}) \right]$$
(11)

Similarly, the voltages at the positions B_i (i = 3, ..., 0) are calculated recursively by the expression:

$$V_{\rm Bi} = V_{\rm B(i+1)} \left[\cos(\beta D_{\rm b(i+1)}) - j \frac{Z_{\rm ob(i+1)}}{Z_{\rm B(i+1)}} \sin(\beta D_{\rm b(i+1)}) \right]$$
(12)

The complex currents applied respectively on the two terminal loads are calculated according to the expressions:

$$I_{\rm AT} = V_{\rm A0}/Z_{\rm AT} \tag{13}$$

$$I_{\rm BT} = V_{\rm B0}/Z_{\rm BT} \tag{14}$$

Finally, the current-split ratio is derived by:

$$CR = |I_{AT}|/|I_{BT}|$$
(15)

Provided that the complex currents $I_{\rm AT}$ and $I_{\rm BT}$ have been calculated, the phase difference $\Delta \varphi$ between these currents can be derived as well.

Since the divider is optimized for multifrequency operation, the BW, CR, and $\Delta \varphi$ must be calculated for all frequencies of operation. The optimization is performed using a PSO algorithm de-

veloped by the authors. For the structure described above, the algorithm uses 16 input parameters, which are the lengths and the corresponding characteristic impedances of the eight sections that compose the divider. The objective of the algorithm is to find the values of the input parameters that maximize a suitably defined fitness function based on the requirements for the BW, CR, and $\Delta\varphi$. Thus, the fitness function is expressed by

$$F = \sum_{m=1}^{M} [w_m^{\text{CR}} |\text{CR}(f_m) - \text{CR}_{d}(f_m)| + w_m^{\Delta\varphi} |\Delta\varphi(f_m) - \Delta\varphi_d(f_m)| + w_m^{\text{RL}} \text{RL}(f_m) + w_m^{\text{BW}} F_m^{\text{BW}}] \quad (16)$$

where *m* is the order of any resonant frequency and *M* is the total number of the resonant frequencies. Moreover, $CR(f_m)$, $\Delta\varphi(f_m)$, and $RL(f_m)$ are respectively the current-split ratio, the phase difference between the currents at the terminal points of the divider, and the return loss, all calculated at the resonant frequency f_m . $CR_d(f_m)$ and $\Delta\varphi_d(f_m)$ are the desired values of CR and $\Delta\varphi$ at f_m . The use of $RL(f_m)$ in Eq. (16) ensures a deep resonance at f_m . The term F_m^{BW} refers to the impedance-matching BW at the resonant frequency f_m , as given below:

$$F_m^{\rm BW} = \begin{cases} BW(f_m) - BW_{\rm d}(f_m), & \text{if } BW(f_m) < BW_{\rm d}(f_m) \\ 0, & \text{if } BW(f_m) \ge BW_{\rm d}(f_m) \end{cases}$$
(17)

where $BW_d(f_m)$ is the desired value of BW at f_m . The meaning of Eq. (17) is that only values of BW less than BW_d have influence on the value of F_m^{BW} . Values of BW greater than or equal to BW_d may not affect the value of F_m^{BW} , because they satisfy the requirement for the BW. In general, F_m^{BW} has negative values and vanishes only when the desired BW is achieved. The coefficients w_m^{CR} , $w_m^{\Delta\varphi}$, w_m^{RL} , and w_m^{BW} are weight factors and they denote the importance of the corresponding terms that compose the fitness function. Provided that w_m^{CR} , $w_m^{\Delta\varphi}$, w_m^{RL} ($m = 1, \ldots, M$) have negative values and w_m^{BW} ($m = 1, \ldots, M$) have positive values, the fitness function tends to be maximized when the BW, CR, and $\Delta\varphi$ tend to reach their respective desired values.

PSO has been studied in many papers [30-39] and is briefly described in [56, 57]. According to the PSO theory, the optimization is based on the intelligence and movement of swarms. Every individual in the swarm is called "particle" and the number S of the particles is called "swarm size." A swarm size of 30 particles is used in the algorithm. The position of the *i*th particle (i = 1, ..., S)is represented as $\vec{x}_i = (x_{i1}, \dots, x_{iN})$, where x_{in} $(n = 1, \dots, N)$ are the position coordinates in a N-dimensional search space. Actually, these coordinates are the 16 earlier-mentioned parameters that define the geometry of the divider (N = 16). Each coordinate x_{in} is usually limited between a lower boundary L_n and an upper boundary U_n . The difference $R_n = U_n - L_n$ is called "dynamic range" of the nth dimension. The position of each particle is evaluated by the fitness function $F = F(\vec{x}_i)$. An increase in the fitness value means that the particle improves its position. Consequently, the best position in the search space (gbest position) \vec{g} $= (g_1, \ldots, g_N)$ is the solution to the optimization problem because it corresponds to the maximum fitness value $F_{\text{max}} = F(\vec{g})$. After a time step, the new position of the *i*th particle is given by

$$\vec{x}_i(t+1) = \vec{x}_i(t) + \vec{v}_i(t+1)$$
(18)

 $\vec{v}_i = (v_{i1}, \dots, v_{iN})$ is the velocity of the *i*th particle and is updated [38] by the expression

TABLE 1Structure Characteristics of the Optimized DividerThat Resonates at 900 and 1800 MHz, and Feeds TwoResistive Loads $Z_{AT} = 60 \ \Omega$ and $Z_{BT} = 72 \ \Omega$

Example 1	Case a ^a	Case b ^b	Case c ^c
Z_{oa1}/D_{a1}	97.03 Ω/0.249 λ ₀	78.89 Ω/0.227 λ ₀	99.37 Ω/0.175 λ ₀
Z_{oa2}/D_{a2}	102.02 Ω/0.272 $λ_0$	85.05 Ω/0.234 λ0	98.13 Ω/0.165 λ ₀
Z_{oa3}/D_{a3}	84.186 Ω/0.155 λ ₀	87.70 Ω/0.281 λ ₀	147.14 Ω/0.211 λ ₀
Z_{oa4}/D_{a4}	103.11 $\Omega/0.232 \lambda_0$	106.61 Ω/0.129 λ ₀	100.74 Ω/0.106 $λ_0$
$Z_{\rm ob1}/D_{\rm b1}$	119.06 Ω/0.312 $λ_0$	108.78 Ω/0.266 λ ₀	65.23 Ω/0.255 λ ₀
$Z_{\rm ob2}/D_{\rm b2}$	131.64 Ω/0.166 $λ_0$	98.16 Ω/0.214 λ ₀	65.75 Ω/0.140 λ ₀
$Z_{\rm ob3}/D_{\rm b3}$	74.52 Ω/0.204 λ ₀	63.42 Ω/0.209 λ ₀	61.96 Ω/0.135 λ ₀
$Z_{\rm ob4}/D_{\rm b4}$	78.68 Ω/0.204 $λ_0$	72.86 Ω/0.180 $λ_0$	76.08 Ω/0.116 λ ₀

^{*a*} For case a: Frequencies: 900 and 1800 MHz; Required CR/ $\Delta\phi$: 1/0° and 1/0°; Resulted CR/ $\Delta\phi$: 1.000/0.0° and 1.000/0.0°, respectively.

^{*b*} For case b: Frequencies: 900 and 1800 MHz; Required CR/ $\Delta\phi$: 1/0° and 1.5/0°; Resulted CR/ $\Delta\phi$: 0.999/0.0° and 1.504/0.0°, respectively.

^c For case c: Frequencies: 900 and 1800 MHz; Required CR/ $\Delta\phi$: 1.5/0° and 1/0°; Resulted CR/ $\Delta\phi$: 1.500/0.0° and 1.000/0.0°, respectively.

$$\vec{v}_{i}(t+1) = k\{\vec{v}_{i}(t) + \varphi_{1} \operatorname{rand}(t)[\vec{p}_{i}(t) - \vec{x}_{i}(t)] + \varphi_{2} \operatorname{rand}(t)[\vec{\ell}_{i}(t) - \vec{x}_{i}(t)]\}$$
(19)

where $\vec{p}_i = (p_{il}, \dots, p_{iN})$ is the best previous position (pbest position) of the *i*th particle, $\ell_i = (\ell_{i\ell}, \dots, \ell_{iN})$ is the best position (lbest position) found so far by the K_i neighbors of the *i*th particle, and rand (*t*) is a uniform random number generator. Three neighbors per particle ($K_i = 3$) is a good choice for many problems. In addition, the parameter *k* is the "constriction coefficient" and is defined by the expression

$$k = \frac{2}{\left|2 - \varphi - \sqrt{\varphi^2 - 4\varphi}\right|} \tag{20}$$

The parameter $\varphi = \varphi_1 + \varphi_2$ is called "acceleration constant" and must be greater than four. A good choice for both φ_1 and φ_2 is 2.05 [36]. Thus, $\varphi = 4.10$ and k = 0.73. To keep the particles in bounds, it is better to define a maximum allowed velocity \vec{v}_{max} $= (v_{max,1}, \dots, v_{max,N})$. Each coordinate of this velocity is set to $v_{max,n} = 0.10R_n$ ($n = 1, \dots, N$). Therefore, for each *i*th particle and



Figure 2 Frequency response of the optimized divider that resonates at 900 and 1800 MHz, and feeds two resistive loads $Z_{AT} = 60 \Omega$ and $Z_{BT} = 72 \Omega$

TABLE 2Structure Characteristics of the Optimized DividerThat Resonates at 900 and 1800 MHz, and Feeds TwoComplex Loads $Z_{AT} = 100 + j15 \Omega$ and $Z_{BT} = 200 - j40 \Omega$

Example 2	Case a ^a	Case b ^b	Case c^c
Z_{oa1}/D_{a1}	92.10 Ω/0.232 λ ₀	75.56 Ω/0.220 λ ₀	136.76 Ω/0.212 λ ₀
Z_{oa2}/D_{a2}	114.76 $\Omega/0.342 \lambda_0$	116.88 Ω/0.262 λ ₀	112.46 Ω/0.152 λ ₀
Z_{oa3}/D_{a3}	107.27 Ω/0.280 λ ₀	153.60 Ω/0.312 $λ_0$	89.63 Ω/0.267 λ ₀
Z_{oa4}/D_{a4}	107.17 Ω/0.225 λ ₀	129.63 Ω/0.285 λ ₀	143.26 Ω/0.217 $λ_0$
$Z_{\rm ob1}/D_{\rm b1}$	129.37 Ω/0.161 λ ₀	157.52 Ω/0.161 λ ₀	133.68 Ω/0.122 λ ₀
$Z_{\rm ob2}/D_{\rm b2}$	101.66 $\Omega/0.354 \lambda_0$	106.52 Ω/0.283 λ ₀	100.27 Ω/0.290 λ ₀
$Z_{\rm ob3}/D_{\rm b3}$	135.69 Ω/0.280 λ ₀	107.00 Ω/0.352 $λ_0$	124.58 Ω/0.270 λ ₀
$Z_{\rm ob4}/D_{\rm b4}$	97.24 Ω/0.295 λ ₀	79.93 Ω/0.289 $λ_0$	101.51 Ω/0.173 λ ₀

^{*a*} For case a: Frequencies: 900 and 1800 MHz; Required CR/ $\Delta\phi$: 1/0° and 1/0°; Resulted CR/ $\Delta\phi$: 1.000/0.0° and 1.000/0.0°, respectively.

^b For case b: Frequencies: 900 and 1800 MHz; Required CR/ $\Delta\phi$: 1/0° and 1.5/0°; Resulted CR/ $\Delta\phi$: 1.000/0.0° and 1.500/0.0°, respectively.

^c For case c: Frequencies: 900 and 1800 MHz; Required CR/ $\Delta\phi$: 1.5/0° and 1/0°; Resulted CR/ $\Delta\phi$: 1.500/0.0° and 1.000/0.0°, respectively

each *n*th dimension, if $v_{in} > v_{max,n}$ then $v_{in} = v_{max,n}$, and also if $v_{in} < -v_{max,n}$ then $v_{in} = -v_{max,n}$. However, \vec{v}_{max} and *k* are not able to confine the particles within the search space. To solve this problem, a boundary condition called "absorbing walls" is used in the PSO algorithm. According to this condition, if $x_{in} > U_n$ then $x_{in} = U_n$ and $v_{in} = 0$, and also if $x_{in} < L_n$ then $x_{in} = L_n$ and $v_{in} = 0$. Finally, a brief description of the structure of the PSO algorithm can be found in [56, 57].

3. RESULTS

The proposed technique was initially applied at the mobile communications frequencies of 900 and 1800 MHz, to optimize the divider for GSM/DCS operation. Two unequal resistive loads Z_{AT} = 60 Ω and Z_{BT} = 72 Ω are assumed to be connected at the terminal points of the divider. Three cases are studied in this example concerning different values of current-split ratio: (a) CR = 1 at both frequencies, (b) CR = 1 at 900 MHz and CR = 1.5 at 1800 MHz, (c) CR = 1.5 at 900 MHz and CR = 1 at 1800 MHz. In all the earlier cases, the currents at the terminal points of the divider are required to be in phase ($\Delta \varphi = 0$). The results of the three cases are summarized in Table 1, while the frequency re-



Figure 3 Frequency response of the optimized divider that resonates at 900 and 1800 MHz, and feeds two complex loads $Z_{AT} = 100 + j15 \Omega$ and $Z_{BT} = 200 - j40 \Omega$

TABLE 3 Structure Characteristics of the Optimized Divider That Resonates at 900, 1800, 1900, and 2050 MHz, and Feeds Two Resistive Loads $Z_{AT} = 60 \ \Omega$ and $Z_{BT} = 72 \ \Omega$

		C 1h
Example 3	Case a"	Case b ^o
Z_{oa1}/D_{a1}	79.34 Ω/0.122 λ ₀	55.40 Ω/0.449 λ ₀
Z_{oa2}/D_{a2}	114.36 Ω/0.186 $λ_0$	69.67 Ω/0.325 $λ_0$
Z_{oa3}/D_{a3}	125.88 Ω/0.268 λ ₀	99.63 Ω/0.330 λ ₀
Z_{oa4}/D_{a4}	97.42 Ω/0.241 λ ₀	150.51 Ω/0.343 λ ₀
$Z_{\rm ob1}/D_{\rm b1}$	80.92 Ω/0.118 λ ₀	71.88 Ω/0.465 λ ₀
$Z_{\rm ob2}/D_{\rm b2}$	97.78 Ω/0.195 λ ₀	72.18 Ω/0.283 λ ₀
$Z_{\rm ob3}/D_{\rm b3}$	98.19 Ω/0.273 λ ₀	71.65 Ω/0.353 λ ₀
$Z_{\rm ob4}/D_{\rm b4}$	84.55 Ω/0.231 λ ₀	69.86 $\Omega/0.345 \lambda_0$

^{*a*} For case a: Frequencies: 900, 1800, 1900, and 2050 MHz; Required CR/ $\Delta\phi$: 1/0°, 1/0°, 1/0°, and 1/0°; Resulted CR/ $\Delta\phi$: 1.000/-0.5°, 0.995/ 0.0°, 1.006/0.0°, and 1.000/0.0°, respectively.

^b For case b: Frequencies: 900, 1800, 1900, and 2050 MHz; Required CR/Δ ϕ : 1.5/0°, 1.5/0°, 1.5/0°, and 1.5/0°; Resulted CR/Δ ϕ : 1.500/0.4°, 1.500/0.0°, 1.500/0.0°, and 1.500/0.0°, respectively.

sponse of the divider is presented in Figure 2. Specifically, the tables of all the examples show the required and the resulted values of CR and $\Delta \varphi$, as well as the characteristic impedances and the lengths of the sections derived from the optimization procedure. All the lengths are measured in terms of a reference wavelength λ_0 , which is the wavelength inside the structure of the divider at 900 MHz. It is obvious that the frequency response of the resulting divider satisfies the requirements for GSM/DCS operation.

The technique was also applied at 900 and 1800 MHz considering two unequal complex terminal loads $Z_{AT} = 100 + j15 \Omega$ and $Z_{BT} = 200 - j40 \Omega$. The same three cases are also studied in this example and $\Delta \varphi$ is required to be equal to zero in all these cases. The results are given in Table 2, while Figure 3 exhibits an excellent frequency response. It is well known that the complex loads are more destructive to the matching condition than the resistive loads. However, the particle swarm optimizer is capable of finding a very broadband structure, which is suitable for GSM/ DCS operation.

The next example is an effort to optimize the divider for GSM/DCS/PCS/UMTS operation. Therefore, the divider must resonate at 900, 1800, 1900, and 2050 MHz. The same unequal



Figure 4 Frequency response of the optimized divider that resonates at 900, 1800, 1900, and 2050 MHz, and feeds two resistive loads $Z_{AT} = 60 \Omega$ and $Z_{BT} = 72 \Omega$

TABLE 4 Structure Characteristics of the Optimized Divider that Resonates at 900, 1800, 1900, and 2050 MHz, and Feeds Two Complex Loads $Z_{AT} = 100 + j15 \Omega$ and $Z_{BT} = 200 - j40 \Omega$

-		
Example 4	Case a ^a	Case b ^b
Z_{oal}/D_{al}	106.68 Ω/0.207 λ ₀	123.03 Ω/0.222 λ ₀
Z_{oa2}/D_{a2}	80.38 Ω/0.285 λ ₀	78.72 Ω/0.289 λ ₀
Z_{oa3}/D_{a3}	90.64 Ω/0.187 λ ₀	93.50 Ω/0.174 λ ₀
Z_{0a4}/D_{a4}	123.69 Ω/0.110 $λ_0$	181.24 Ω/0.145 λ_0
$Z_{\rm ob1}/D_{\rm b1}$	$135.52 \Omega/0.111 \lambda_0$	155.44 Ω/0.093 λ ₀
$Z_{\rm ob2}/D_{\rm b2}$	77.24 Ω/0.184 λ_0	112.08 Ω/0.252 λ ₀
$Z_{\rm ob3}/D_{\rm b3}$	61.90 Ω/0.275 λ ₀	100.82 Ω/0.361 λ ₀
$Z_{\rm ob4}/D_{\rm b4}$	84.02 Ω/0.239 λ ₀	74.97 $\Omega/0.134 \lambda_0$

^{*a*} For case a: Frequencies: 900, 1800, 1900, and 2050 MHz; Required CR/ $\Delta\phi$: 1/0°, 1/0°, 1/0°, and 1/0°; Resulted CR/ $\Delta\phi$: 1.000/0.0°, 1.000/0.0°, 1.000/0.0°, 1.002/-2.1°, and 1.000/0.0°, respectively.

^b For case b: Frequencies: 900, 1800, 1900, and 2050 MHz; Required CR/Δ ϕ : 1.5/0°, 1.5/0°, 1.5/0°, and 1.5/0°; Resulted CR/Δ ϕ : 1.500/0.4°, 1.500/0.0°, 1.498/-2.1°, and 1.500/0.0°, respectively.

resistive loads $Z_{AT} = 60 \Omega$ and $Z_{BT} = 72 \Omega$ are assumed to be the terminal loads of the divider. Two cases are studied in this example: (a) CR = 1 and $\Delta \varphi = 0$ at all the above four frequencies, and (b) CR = 1.5 and $\Delta \varphi = 0$ at all the above four frequencies. The results are summarized in Table 3, while the frequency response of the divider is presented in Figure 4. Despite the fact that the terminal loads are not matched to the main feeding line, the optimization procedure is capable of finding a broadband structure, which is suitable for GSM/DCS/PCS/UMTS operation.

The last example is another effort to optimize the divider for GSM/DCS/PCS/UMTS operation, considering two unequal complex loads $Z_{AT} = 100 + j15 \Omega$ and $Z_{BT} = 200 - j40 \Omega$ connected at the terminal points of the divider. The two cases studied in the previous example are also studied here. The results are given in Table 4, while Figure 5 exhibits a very broadband structure that satisfies the requirements for GSM/DCS/PCS/UMTS operation.

4. CONCLUSIONS

Despite the initial requirements for GSM/DCS or GSM/DCS/PCS/ UMTS operation, our technique results in very broadband struc-



Figure 5 Frequency response of the optimized divider that resonates at 900, 1800, 1900, and 2050 MHz, and feeds two complex loads $Z_{AT} = 100 + j15 \ \Omega$ and $Z_{BT} = 200 - j40 \ \Omega$

tures. Moreover, the technique has the ability to satisfy simultaneously many requirements at many frequencies, considering that the terminal loads are not matched to the main transmission line that feeds the divider. Therefore, the efficiency of the proposed technique exceeds our expectations. In addition, the proposed structure of the divider is compact and simple. The optimized structures can be fabricated by using microstrip technology, and thus they can be effectively used in many practical applications.

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TUNABLE (BA, SR)TIO₃ INTERDIGITAL CAPACITOR ONTO SI WAFER FOR RECONFIGURABLE RADIO

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ABSTRACT: In this article, the potential feasibility of integrating BST films into Si wafer by adopting tunable interdigital capacitor (IDC) with

TiO₂ thin film buffer layer is suggested. TiO₂ as buffer layer is grown onto Si substrate by atomic layer deposition and the coplanar IDC on $Ba_xSr_{1-x}TiO_3(500 \text{ nm})/TiO_2(50 \text{ nm})/high resistivity Si (HR-Si) is fabri$ cated. BST interdigital tunable capacitors integrated on HR-Si substratewith high tunability and low loss tangent are characterized for their $microwave performances. BST/HR-Si and BST/TiO_/HR-Si IDCs show much$ enhanced tunability values of 31% and 40%, respectively as compared tothe value of 21% obtained with BST film on MgO single crystal substrate at $the bias of 5 kV/cm. BST/TiO_/HR-Si structure shows much improved figure$ of merit of 504.4 as compared to 418.53 and 101.68 of BST/MgO and BST/HR-Si structure, respectively. © 2007 Wiley Periodicals, Inc. MicrowaveOpt Technol Lett 49: 2144–2148, 2007; Published online in Wiley Inter-Science (www.interscience.wiley.com). DOI 10.1002/mop.22657

Key words: *tunable capacitor;* $Ba_xSr_{1-x}TiO_3$; *ferroelectric; interdigital capacitor*

1. INTRODUCTION

In the recent wireless communication system field, a great diversity of networks such as cellular network, personal area network, wireless local area network, satellite, ultra-wide band, ubiquitous, and optical network coexist. To realize global roaming, both for voice and data application, all of these standards need to be included in a various service radio terminal. Therefore, a compact and low cost tunable device is indispensable for intelligent RF applications satisfying multi-band or multi-mode standard. Tunable dielectric-based RF device technology is one of the suitable solutions for intelligent RF applications. Specially, barium strontium titanate, $Ba_{1-r}Sr_rTiO_3$ (BST), is being investigated with considerable interest as a dielectric material for tunable microwave device applications because of its large field dependent permittivity, high dielectric constant, and relatively low loss tangent. By using these advantageous properties of BST, a number of advanced high frequency tunable capacitors have been successfully demonstrated and integrated into RF components, such as phase shifters, RF filters, and oscillators, because of its large field dependent permittivity, high dielectric constant, and relatively low loss tangent [1, 2]. Today, the fabrication of BST-based tunable devices have been done with only available in a small size geometry and single crystal substrates such as MgO and LaAlO₃, which can provide low insertion loss, good lattice match, and good mechanical support. As microwave integrated circuits largely rely on Si technology with its low cost, large area, and high volume production, there are of significant interests for BST based high frequency devices onto Si substrate [3]. However, practical applications of BST tunable devices on Si substrates are suppressed owing to the high microwave losses related to the low resistivity of Si and formation of low-K SiO2 by the reaction between the top BST and Si substrate. In addition, high microwave insertion losses related to low resistivity Si have served as a barrier against the realization of BST and related device integration with Si microelectronics. To solve these problems, first, high resistivity Si (HR-Si) is required as the substrate for the minimization of microwave insertion losses. Second, suitable oxide buffer layers are required between the top BST layers and Si substrates to control the orientation and quality of the BST films. Our previous work, which was focused on the low-frequency dielectric properties of BST film grown on a Si substrate with a TiO₂ buffer layer grown by atomic layer deposition (ALD) at a low temperature of 220°C, demonstrated that the TiO₂ buffer layer significantly increased the tunablity of the BST film and minimized power loss via the Si substrates [1, 2].

In this article, we report on high tunability and improved microwave loss properties of BST films on HR-Si by the insertion of ALD-grown TiO_2 buffer layer. The nonlinear dielectric prop-