

ACCELERATOR DESIGN WITH OPENCL

(ATHENS WEEK 19-24 MARCH, 2018)



PREREQUISITES



- Computer Archietcture
- VLSI
- C/C++



PREREQUISITES (LAB WORK)



- Makefile
- Git
- Linux Command Line
- Python



COURSE PLAN

Session Topic

19/03 10:00 Introduction/Setup

19/03 13:30 GPU/SoC Architecture Recap

20/03 8:30 Lab Work With ARM MALI / ODROID XU4

20/03 8:30 Lab Work With ARM MALI / ODROID XU4

20/03 13:30 Lab Work With ARM MALI / ODROID XU4

21/03 8:30 Project: Video Filtering



COURSE PLAN

Session Topic

21/03 13:30 FPGA/SoC Architecture Recap

22/03 8:30 Lab Work Altera DE1-SoC

22/03 13:30 Lab Work Altera DE1-SoC

23/03 8:30 Project: Video Filtering

23/03 13:30 Quiz & Corrections



PRINCIPLES

- **Provenance:** Always verify and cite the source of Information.
- **CopyPaste:** Do not copy code/commands from forums without understanding them.



EVALUATION



- Lab work. At the end of the week you should submit functional code. (10)
- There will be a multiple-choice quiz on friday. (10)



LET'S SETUP

- PCs in this lab a405-xx.enst.fr
- you can change the default language at the startup screen

