



Research Report 2005-2009

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Télécom ParisTech / LTCI



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commu- -nication

Communications and Electronics

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Télécom ParisTech / LTCI

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Communications and Electronics (COMELEC)

The research led in the **Communications and Electronics** department is devoted to the physical layer of ICT (Information and Communication technology). A useful concept for depicting the department main research concern is that of “physical information”, where the information content is actually reached through some physical properties and manipulated using physical laws such as Maxwell electromagnetic equations or Quantum Hamiltonians. The department covers both the field of communication and that of information processing (electronics).

The department accounts for 36 permanent research staff and hosts about 90 non permanent researchers, including PhD students. The research activity is covered by four different teams. While fixed communications are dealt with by the **Optical communication group**, the **Electronics and RF systems team** concentrate on the transformation from analog to digital information and to its transmission through wireless means. The **Digital communications team** works on the digital coding of the information, and prepare for the future digital communication breakthrough in MIMO system, multi-hop communications or multi-users wireless communications. Processing information requires extremely sophisticated Silicon chips (processors, FPGAs, SOCs), the architecture of which is central to the **Complex Digital Electronics system team**. Transverse to all these activities, one may also find security as a main topic.

The department research policy claims for a research effort that spreads from fundamental physics to applied results. One may for example note the use of quantum dots for optical clock recovery (see the optical communication team) or that of photonic crystal (also called metamaterial) for advanced antennas (Electronics and RF Systems team). The balance between exploratory research and market oriented results is well expressed by the 1:4 ratio between our private partners funding and our total research contract income (7 Meuros cumulated over the period). Because of an innovation minded research taking its roots in fundamental theories, the department was granted 29 patents while publishing over 550 papers in journals and conferences in the evaluation period.

The department is also strongly involved in educating students for research. This is reflected by the 70 defended PhD thesis over the period. A budget of about 75 keuros is also spent yearly for master student internships in the department research groups, with a total of about 150 man.month of internship generated every year. Besides its contribution to the “ingenieur courses” of TELECOM ParisTech, the department researcher’s participate to master courses with ParisVI, ParisXI, as well with the University of Nice.

In terms of outreach, the department was strongly involved in a number of initiatives both at local, national and international levels. Among other actions, one may notice the Electronics and RF systems team involvement in the creation of the GIS Esys “Groupement pour l’Electronique des Systèmes” led by Supelec. The Complex Digital Electronics system team initiated the Sophia-Antipolis Formal Analysis group SAFA and recently animated its first workshop. The Electronics and RF systems team was also a recognized actor in the launching of the joint IEEE Newcas-TAISA conference. Of interest, a world open contest on electronics attacks was launched by the Complex Digital Electronics system team at the CNESS meeting. In order to help for a better European visibility, the department head created and chaired the IDEA League (Imperial College,

Delft University, Eth Zurich, Aachen RWTH) ICT cluster. As an international impact indicator, the department teams are currently involved into two European STREPS, three European NOE's, one Eureka program and lead a Carnot-Fraunhofer project.

Faculty [IT, CNRS]	[29.75, 2.75]
PhD students	48.5
Post-docs, engineers and sabbaticals	10.75
Defended PhD theses	70
Defended HDR	4
Journal papers [published, in press]	[143, 18]
Papers in conference proceedings	410
Chapters and books	10
Patents and software	[29, 5]
Grants [public, private, european] (k€)	[4731, 1902, 417]

Chapter 1

Digital Communications (COMNUM)

Team leader Jean-Claude Belfiore (P).

Faculty

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Faculty [IT, CNRS]	[5.8, 0.7]
PhD students	13
Post-docs, engineers and sabbaticals	1.3
Defended PhD theses	22
Defended HDR	1
Journal papers [published, in press]	[38, 6]
Papers in conference proceedings	98
Chapters and books	3
Patents	10
Grants [public, private, european] (k€)	[377, 512, 95]

1.1 Objectives

The Digital Communication team is traditionally working in what is called the physical layer of a network, that is, how to reliably transmit data from one point to another one. In order to achieve that, many techniques are used,

- Information theory to establish the fundamental limits of the system
- Signal processing to address problems related to synchronisation, channel estimation, modulation techniques such as OFDM or Ultra WideBand
- Channel Coding covering two aspects. One of them is the traditional channel coding with redundancy and the other one uses tools from number theory in order to find the right geometrical transforms that will give diversity in wireless systems.

During the last years many evolutions of this area have given rise to the study of a more complex problem than the point-to-point communication. It has started with the use of multiple antennas at both the transmitter and the receiver, the so-called MIMO systems.

Starting from this problem, many other ones appeared among which cooperative communications, multi-user communication systems or more generally, wireless networks. Many nodes in a wireless network want to communicate data to other nodes helped by a third group of nodes (through multihop communication). The digital communications team has followed this evolution by working on MIMO systems, on cooperative systems and on multi-user communications where we consider many data flows instead of a single one.

A last part is devoted to statistics for communication seen as a tool for analysis and for parameter estimation.

Notice that our research activities are always supported by national, european or industrial fundings. In addition to these activities, the team has an intense activity in terms of teaching for the Engineering school as well as for Research Masters. We especially are the leader for the Research Master, called ESCO/STN, in collaboration with Université Pierre et Marie Curie.

1.2 Main Results

The main research results obtained during the period 2005-2009 are presented below.

1.2.1 Coding and Decoding for MIMO Systems

Faculty J.-C. Belfiore, J. Boutros (–07/07), G. Rekaya - Ben Othman

Main events Organisation of NEWCOM Autumn School on “Space-Time Coding” which took place in Turin in 2006 with 25 attendees. Jean-Claude Belfiore was the recipient of “Médaille Blondel” in 2007 for the invention of “Golden Codes”. Ghaya Rekaya Ben Othman was the recipient of “Jeune Chercheuse parisienne” award in 2006.

Projects ANR XCODES, CIFRE MITSUBISHI, CIFRE THOMSON

Space-Time Coding Space-Time coding techniques have been successful and widely used these last years. Some Space-Time codes have been integrated in several standards like the 3GPP (HSDPA), the Wifi (standard IEEE 802.11n) and Wimax (standard IEEE 802.16e). Our team is widely recognized in the world for its works on Space-Time codes. After the celebrated “Golden Code”, proposed for 2 transmit antennas MIMO systems, we have extended this construction to a larger number of transmit antennas, using the fantastic tool of cyclic division algebras [148]. These new codes have been called “Perfect Codes” [36], and include the Golden Code as a special case. In the context of impulse Ultra WideBand (UWB), the complex envelope of the transmitted signals is real. We have developed real Space-Time codes adapted to the UWB context [3, 1, 5, 4]. A new space-Time code called the “Silver Code” has been recently proposed for the 2×2 MIMO channel. This code has a low decoding complexity compared to the Golden code. We have found the algebraic structure of this code (an ideal of an order of a cyclic division algebras) [118].

Variations on the Golden Code Space-Time codes were constructed to exploit all the degrees of freedom of the MIMO channel, in terms of diversity and multiplexing gain. In practical applications, these codes are concatenated with error correcting codes such as turbo-codes or LDPC. To analyze the performance of these codes in a practical context, we have studied the integration of the Golden Code in the MIMO-HSDPA [108] and in the WiFi 802.11n [101]. For the MIMO-HSDPA, it was proven that the Golden code offers the best performance compared to the best scheme having the same rate and the same diversity order. For the Wifi, the Golden code have almost the same performance as the other codes, this is explained by the high frequency diversity provided by the convolutional code compared to the diversity provided by Space-Time Code. We have also considered the case of correlated antennas at the emission side. The performance of the Golden code in this context are deteriorated. We have so proposed a linear precoder taken into account the correlation [122]. We have also studied the use of the Golden code and the 4×4 Perfect code in the case of very slow fading channel (channel constant during more than 50 time slots). We have proposed a partition of the Golden code and the 4×4 Perfect code function of the minimum determinant, and a coding and decoding scheme concatenating the Space-Time Code with Treillis codes [127, 23]. These partitions provide an important gain compared to the simple Golden Code. We was also interested to the use of the Golden code in the case of slow fading channel (channel constant during less than 50 time slot). We have propose in this case a coded modulation scheme combining the Reed-Solomon code and the Golden code that maximize the minimum determinant [33]. Very good performance was obtained compared to the simple Golden code.

MIMO Decoding Algorithms MIMO (even distributed) schemes with or without linear Space-Time codes have a lattice representation which allow their decoding using lattice decoders. The most known and used lattice decoders in the literature are the sphere decoder and the Schnorr-Euchner algorithm. Both decoders offer ML performance but have a complexity which dramatically increases with the lattice dimension and constellation size. We have studied the sequential algorithm called “stack decoder”, which is a tree search algorithm under a cost function constraint, originally proposed in the literature to decode convolutional code. We have proposed a

new decoder, called “SB-stack”, which uses the tree search strategy of the stack decoder (best first search strategy) and the search region of the sphere decoder (a sphere centered on the received signal). This new algorithm have ML performance, but is 30% less complex than the sphere decoder. By introducing a bias parameter (b) in the cost function, the SB-stack offers a range of performance going from ML ($b = 0$) to ZF-DFE ($b \rightarrow \infty$), with decreasing complexity. We also modified the SB-stack to get soft outputs, necessary if the Space-Time code is concatenated with channel coding. A patent on the SB-Stack has been registered [159]. In practical application, a variable decoding time could be a big problem, for example for real-time applications. To solve this problem we have proposed an adaptive decoding scheme, giving rise to another patent [158]. The idea of this adaptive decoding is to choose the most appropriate decoder (optimal or sub-optimal) as a function of the channel quality and the desired performance. We observed, through simulation results, that the decoding complexity remains constant for all channel realizations and for all signal to noise ratios.

Up to now, decoding of algebraic space-time codes has been performed using their lattice representation. The algebraic structure of the code has completely been ignored at the receiver side. We have been able to exploit the algebraic structure of the code even at the decoding side. We have proposed a new decoding method for 2×2 space-time codes constructed from quaternion algebras (like the Golden code) which directly exploits the multiplicative structure of the space-time code in addition to the lattice structure [120]. This method, called “algebraic reduction”, consists in making the code absorbing most of the channel. This is done by approximating the channel matrix by a unit of a maximal order of the associated quaternion algebra. In the quaternionic case, the Swan algorithm can be used to find a finite set of generators of the unit group, by considering its action on the hyperbolic 3-space H_3 . Supposing that this set is known, we develop a searching algorithm to find the best approximation of the channel matrix as a product of these generators. For the Golden Code case, simulation results show that using MMSE-GDFE left preprocessing, the performance of algebraic reduction with ZF decoding is within 3dB of the ML. However the complexity of the algebraic reduction is negligible compared to the ML decoder. A patent on this algebraic reduction has been registered [157].

1.2.2 Multihop Communications

Faculty J.-C. Belfiore, P. Ciblat, W. Hachem, G. Rekaya - Ben Othman

Main events Jean-Claude Belfiore was Technical Track Chairman for conference IEEE PIMRC in 2008. Jean-Claude Belfiore was Publications Chairman for conference IEEE ISIT in 2007.

Projects ANR RADIC-SF, ANR ORIANA, Pôle SYSTEMATIC/ URC Project, CRE FRANCE TELECOM, CIFRE FRANCE TELECOM

New Protocols for Relaying Schemes In the literature two main kinds of relaying protocols exist: Amplify-and-Forward (AF), and Decode-and-Forward (DF). Some other approaches, such as Dynamic DF (DDF) or Compress-and-Forward (CF), have been introduced in the literature, but we did not consider them in our work because of their high computational load. We remind that the relay only applies a linear operator on the receive signal before to re-transmit it in AF based protocols. In DF based protocols, the relay attempts to decode the data, and re-encodes them if data have been decoded. We have proposed two significant improvements to such protocols.

Amplify-and-forward cooperation is an attractive scheme probably because of its low relaying complexity and its nature of linearity. We have shown that it is indeed efficient and optimal in various scenarios as far as the diversity is concerned. Using the diversity-multiplexing tradeoff (DMT) as our evaluation tool, two scenarios are considered : the large network scenario and the small network scenario. In small networks, the non-orthogonal amplify-and-forward (NAF) scheme has been first studied. It has been generalized to the MIMO case, where upper and lower bounds on the DMT the NAF scheme have been derived [43]. The same NAF has been successfully used on UWB systems [6]. All the known half-duplex cooperation schemes, including

both the class of decode-and-forward and amplify-and-forward schemes, are inefficient in the high multiplexing gain regime. With multiple relays, we have shown that the diversity gain can be improved by letting as much the source signal as possible be forwarded by the relays. A simple sequential slotted amplify-and-forward scheme has been proposed [44]. It is shown that this scheme tends to the cut-set bound in some particular cases when the number of slots goes to infinity. The proposed AF cooperation schemes have equivalent MIMO or parallel MIMO channels representation. Both construction criterion and implementation of approximately universal codes have been developed [43, 138].

Concerning DF, we propose a half-duplex single-relay protocol called *Decode or Quantize and Forward* (DoQF). The added quantification step occurs when the relay did not succeed to decode its receive signal. Instead of being silent, the relay sends a quantized version of its receive signal to the destination. The new protocol has been analysed over slowly fading wireless channels. In this context, a relevant performance index is the so-called Diversity gain-Multiplexing gain Tradeoff (DMT). We proved that the DMT of the proposed DoQF relaying protocol, which is a static protocol characterized by a practical receiver structure, achieves the 2×1 Multiple Input Single Output (MISO) upperbound for small multiplexing gains. DoQF protocol thus outperforms the classical non orthogonal Decode-and-Forward (NDF) protocol in terms of DMT. To prove the benefit of the proposed method, we also derived the outage gain which is defined as follows: the term $\rho^2 P_o$ converges to a constant ξ when the SNR ρ tends to infinity and N represents the number of relays. This constant ξ will be referred to as the outage gain. We showed that the DoQF protocol is optimal in terms of outage gain in a wide class of half-duplex relaying protocols [58]. We also studied the outage probability behavior in the context of multi hop communications where the relays are not synchronized. AF as well as DF protocols were considered [105].

Power and Time Optimisation in Relaying Schemes In the context of cooperative wireless networks that convey data on slow fading channels, several protocols that define how the source the relays and the destination have to operate, have been proposed in the literature. One can mention, as already done above, AF, NAF, SAF (proposed by our team), DF, NDF, DoQF (proposed by our team), DDF, etc. When the channels realization are unknown at transmitter sides, these protocols have been extensively analysed in terms of DMT. Nevertheless DMT criterion does not provide insights about the total power distribution between the source and the relays since this criterion is insensitive to that distribution. Moreover each frame defined in any protocol is divided into several time slots. Often these time slots have equal durations for sake of simplicity. Our main contribution was to provide solutions to the optimization problem of power distribution and time slots durations. To do that, as DMT criterion is not efficient, we suggested to only focus on the outage probability P_o when the required data rate is fixed. However, it is often hard to derive a closed-form expression for P_o valid for any value of the Signal to Noise Ratio (SNR). The problem can be simplified by studying the behavior of P_o in the asymptotic regime where the SNR ρ converges to infinity. In this regime, usually $\rho^{N+1} P_o$ converges to a constant ξ where N is the number of relays. We have proposed a simple and general method for deriving and minimizing ξ with respect to the power distribution between the source and the relays, and with respect to the time slots durations specified by the relaying protocol when transmitters only have a statistical knowledge of propagation channels. While the proposed approach is designed for the high SNR regime, we showed that outage probability is reduced in a similar proportion at moderate SNR. Notice that AF, NAF, DF, NDF, and DoQF have been handled. Moreover the method applies to a general class of radio channels that includes the Rayleigh and the Rice channels as particular cases. Last but not the least, we proved that ξ is convex with respect to the design parameters which leads to a simple optimization algorithm [22].

Analysis of Multi-Hop Communication Schemes Without Direct Link Notice that all previous works have done under the assumption that a link (even weak) exists between the source and the destination. When this assumption is not satisfied, a lot of previous works fall down, and a new analysis has to be done. In large networks, requiring the relay terminals to decode the source

message imposes a harsh constraint and limits the achievable multiplexing gain in general, especially when the source and the destination have multiple antennas. A naive amplify-and-forward scheme is space-only processing that achieves the maximum multiplexing gain but suffers from diversity loss. By introducing a simple temporal processing, a flip-and-forward scheme achieves both the maximum diversity and maximum multiplexing gain provided by the channel. It is the best known cooperative scheme in this scenario, in terms of the DMT [46].

1.2.3 Multi-User Communications

Faculty J.-C. Belfiore, P. Ciblat, W. Hachem, G. Rekaya - Ben Othman

Main events Philippe Ciblat served as Associate Editor for IEEE Communications Letters during the period 2004-2007.

Projects ANR RISC, CIFRE THALES, CIFRE MOTOROLA, CRE FRANCE TELECOM, Network of Excellence NEWCOM and NEWCOM++

Analysis of Impulse Radio UWB in Multi-User Environment In multi-user environment, the rake receiver for UWB modulated signals offers poor performance because of the multi-user interference which may be significant. One mean to reduce the level of multi-user interference is to design the multi-user codes properly. Therefore, we have focused intensively on the characterization of the “good” multi-user codes and of the “bad” multi-user codes in impulse radio UWB based on either time-hopping (TH) or direct sequence (DS) multiple access.

First of all, we showed that the multi-user interference assuming fixed multiple access codes can be well approximated by a Generalized-Gaussian Distribution whatever the multipath channel model. Then, thanks to this approximation, we derived an accurate closed-form expression for an approximation of the error probability in both TH and DS multiple access context. Note that the analytical expression for the error probability depends on the real multiple access code.

Secondly, from this error probability approximation, we were able to characterize and to select the set of codes minimizing the error probability for both multiple access techniques. Notice that the set of codes minimizing the power of the multi-user interference is an uperset of the set of codes minimizing the error probability. The merit of each multiple access technique has been then analyzed: we especially proved that the probability to find an optimal pair of codes goes to one when increasing the number of chips per symbol with TH technique whereas this probability goes to zero with DS technique. Therefore we advocated to employ TH technique rather than DS technique [92]. The study of a MIMO multiuser system has been proposed in [4].

Analysis of OFDMA Based Systems in Multi-Cell Environment In the context of Frequency-Hopping OFDMA with a frequency reuse factor equal to one, we have analysed the influence of the multi-cell interference on the performance for the downlink situation. We have assumed that the channel is unknown at the transmitter, but that the channel statistics are available. Furthermore, as frequency-hopping scheme is carried out, we have considered that the ergodic capacity was a accurate approximation of the achievable data rate. Firstly, under the assumption that the base stations combat the multi-cell interference by increasing their own power in order to satisfy the target data rate of their users, we have shown that it exists one power value for which the multi-cell system is stable if the target data rate are lower than a certain value playing the role of a “capacity”. Moreover the subcarrier and power assignment per user has been optimized [16]. Secondly, to be more realistic and to satisfy the recommendation of Wimax forum, we then assumed that a certain part of the available bandwidth may be reused by different base stations (and is thus subject to multicell interference) and that an other part of the bandwidth is used by one base station only (and is thus “protected” from multicell interference). In such a context, we proved that all the subcarriers of one user will be either on the protected bandwidth or on the shared bandwidth. We thus proved that the naive idea of separating bandwidth into two classes

is optimal ! Thanks to an asymptotic analysis for which the number of users was considered to tend to infinity, we were also able to characterize the optimal frequency reuse factor in closed-form [95].

We have studied power and frequency allocation in a distributed OFDMA cellular context [38]. We have established a convergence criterion in the SISO case [37], which has been extended to the MIMO case [114], for rate constrained users. This study has been done for perfect Channel Side Information at transmitters (**CSIT**), but also for statistical CSIT, for which a new expression of the outage probability has been derived. Then, the case of Best efforts users has been considered, still for OFDMA distributed cellular networks. In the literature, only the case of the high SNR regime has been considered. We gave a new method for.

Resource Allocation in OFDMA Based Systems Multi-user systems based on OFDM are frequently used in powerline or quasi-static wideband wireless channels. Typical scenarios assume that precise channel state information can reasonably be obtained at the transmitter and at the receiver. In downlink, a spectral mask constraint is usually imposed too. In such previous context, we assumed two multiple access schemes, MC-DS-CDMA and a simpler OFDMA, and we have investigated their achievable-rate regions. In particular, we studied the so-called “balanced rate criterion”, in order to select a point of the achievable-rate region which guarantees fairness among all the active users. We proposed simplified algorithms to calculate an approximate balanced rate solution for the OFDMA case. The loss of the OFDMA solution with respect to the MC-DS-CDMA solution is shown to be acceptable [68].

Analysis of Broadcast Channel and Multiple Access Channel for Slowly Varying Channels

We have been the first to design space-time codes for the multiple access channel with non cooperative transmitters and no channel side information at the transmitters. These codes have been found, first for single antennas transmitters [53], and then they have been generalized to the case of multiple antenna transmitters [55]. These codes have been shown to achieve between 6-9 dBs gain compared to time sharing. There are based on number fields and show a non zero minimum determinant when considering all users in error. Finally, the Multiple Access codes have been extended to the case of the Multiple Access Relay Channel (**MARC**) [54].

1.2.4 Statistics for Communications

Faculty P. Ciblat, W. Hachem, A. Tchamkerten

Main events Organisation of NEWCOM Autumn School on “Estimation Theory for Wireless Communications” which took place in Paris in 2005 with 85 attendees. Walid Hachem and Philippe Ciblat have been Associate Editor for IEEE Transactions on Signal Processing since 2007 and 2008 respectively.

Projects ANR DEMAIN, CNRS/ACI MALCOM, CIFRE THALES, ANR SESAME, Royal Society fellowship

Optimal Training for Channel and Frequency Offset Estimation In the context of digital communications, in order to estimate the channel impulse response and also the Carrier Frequency Offset (CFO), a training sequence is sent periodically by the transmitter to the receiver. Of interest is the statistical description of the best training sequence and this can be done by minimizing the Cramer-Rao Bound (CRB). In this situation, most existing works proposed to average the CRB associated with the CFO over the channel statistics. But these works assumed that the channel components are independent and identical distributed (iid), which is a rather restrictive assumption. With this assumption, they established that the best training sequence is the pseudo-random white sequence. But, in practice, the iid hypothesis is often restrictive. We first

revisited the problem of training design for CFO estimation (considering correlated channel components) in the context of OFDM systems. In this situation, we unveiled the trade-offs that govern the optimum training sequence design, and showed that there exist training power profiles which are better than the uniform profile [17, 39]. We then addressed the more difficult problem of designing training sequence for joint estimation of the channel and the carrier frequency offset joint but in a single-carrier setting. Since in general the training sequence optimizing CFO estimation is not optimum for channel estimation, we have designed the training sequence that minimizes the Mean-Square Error on the soft estimates of the data symbols obtained using a Wiener equalizer after CFO compensation and averaged over channel statistics. We thus found an “optimal” training sequence, relevant for channel estimation and CFO estimation. Correlated and/or Ricean channel taps have been considered. We showed a significant gain on Bit Error Rate when using the proposed training scheme in lieu of white training [10].

System Recognition for Cognitive Radio In the context of cognitive/opportunistic radio, the signal central frequency does not characterize the used system any more since a system may choose its band in an opportunistic manner. In order to reduce latency, only short signal record has to be considered which may be unfortunately preamble-free. Therefore it is a crucial task to distinguish blindly various OFDM based systems (e.g., Wifi, Wimax, 3GPP/LTE, DVB-T) from each others. We have proposed two main approaches to fix the above-mentioned problem.

First of all, recognition algorithms can be based on the subcarrier spacing value, a quantity which is in general specific to a given OFDM system. Standard approaches rely on the detection of the cyclic prefix which directly provides subcarrier spacing value. Nevertheless these approaches fail when either the cyclic prefix duration is small or the channel impulse response is almost as large as the cyclic prefix. We have thus proposed four new subcarrier spacing estimation algorithms robust to short cyclic prefix and multipath channel [63, 61, 60, 62]. Secondly, we have presented two new OFDM system identification methods based on the structure (not the deterministic value) of the pilot tones. The first proposed scheme relies on the cyclostationarity property induced by the existing structure in time/frequency of the pilot tones. The second one is based on the characteristic of various maximum length sequences used to generate the pilot tones [129, 130].

Channel Estimation Performance for UWB Based Systems In the context of impulse radio UWB, we have addressed the CRB calculation for channel parameter estimation. We have considered a time-hopping code scheme with binary pulse position or pulse amplitude modulation formats. We derived in closed-form the (resp. modified) Cramer-Rao bound for the multipath channel parameters in the data-aided (resp. non-data-aided) context. Unlike existing methods, the calculations have been derived by taking into account the overlapping between signal echoes due to multipath. We showed that it is important to consider the overlapping assumption on realistic channel propagation environment since the Cramer-Rao bound using the non-overlapping assumption clearly overestimated the performance [11].

Tracking Stopping Times Through Noisy Observations Here we investigated a decision problem, the tracking stopping time (TST) problem, whose range of applications spans communication, detection, forecasting, quality control, and finance. The problem is stated as follows. Consider a sequence (X_i, Y_i) of pairs of random variables. At time i Alice observes X_i and Bob observes Y_i only. At a certain time that depends on her previously observed symbols, Alice makes a decision. The goal of Bob is to estimate at best Alice's decision time based only on his observations. In spite of the simplicity of its statement, the TST problem admits no general solutions. In collaboration with Marat Burnashev from the Russian Academy of Sciences in Moscow, we investigate decision time estimates that are provably close to optimal when the X and the Y sequences are highly correlated Brownian motions. Recent related results can be found in [35] and [106].

Large Random Matrix Theory and Wireless Communications The study of the spectral behavior of random matrices when both matrix dimensions converge to infinity at the same pace leads to a deeper understanding of the performance of MIMO systems, multi user receivers, and wireless radio networks. In this context, we studied the distribution of the eigenvalues of a class of large matrices with non centered and correlated entries, and based on this study we characterized the Shannon mutual information of a general class of Ricean correlated MIMO channels [19]. We also proposed an optimization technique for the transmitter covariance matrix in order to attain these channels Shannon's capacity [76]. We also studied the convergence behavior of the $\log \det$ functional (Shannon's mutual information of MIMO channels) [20, 21], as well as the Signal to Noise Ratio at the output of multi user receivers [26], mostly under the form of Central Limit Theorems. These theorems lead to pertinent outage probability approximations.

Performance Detection for Wireless Sensor Networks In the context of wireless randomly located sensor networks intended to detect a 1D or 2D signal (temperature, moisture, ...) often the detection error probability decreases exponentially in the number of sensors. The characterization of the error exponent leads to interesting guidelines as regards the optimum sensor distribution given a correlation structure of the field to be detected. Beginning with the 1D case (detecting a signal by performing a random sampling) we characterized these exponents when the continuous parameter signal is described by a class of scalar or vector stochastic differential equations. We are currently seeking to generalize these findings to the 2D case (detection of a random field).

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Chapter 2

Complex Digital Electronic Systems (SEN)

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Associate Researchers

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Sabbaticals

N. Homma (Associate Professor, Tohoku University Japan, 06/09–),

Faculty [IT, CNRS]	[8.5 , 2]
PhD students	8
Post-docs, engineers and sabbaticals	5.5
Defended PhD theses	7
Defended HDR	1
Journal papers [published, in press]	[17, 2]
Papers in conference proceedings	86
Chapters and books	3
Patents and software	[9, 4]
Grants [public, private, european] (k€)	[2747, 458, 164]

2.1 Objectives

The “Complex Digital Electronic System” team research topics are oriented towards efficient design of digital electronic systems. The team’s research is based on the development of new algorithms, new architectures and new methods taking into account recent and future integrated technologies evolutions. The objectives are closely linked to the study of cutting edge techniques which allows electronic designers to meet ever growing constraints like complexity management, reliability, consumption, speed, and flexibility. Application focus on wireless digital communications, multimedia processing and security (trusted computing hardware).

The team has regular research collaborations with well known companies of the area such as STMicroelectronics, NXP, Freescale, ST-Ericsson, CEA, Orange or TexasInstruments. Among the academic laboratories the most representative are the CNRS laboratories LIP6 (UMR7606), LIRMM (UMR5506), GIPSA-Lab (UMR5216) or TIMA (UMR51599). We are also highly involved in the CIM (Centre Intégré de Microélectronique) PACA regional framework and the SAME (Sophia Antipolis MicroElectronics) association, two very important academic-industrials French consortiums. In order to balance industrials concerns and advanced academic research, we increased during the two past years our effort for PhD supervision. We focused our research towards four main themes. Design methodologies are covered by the **Design Space exploration and assisted refinement of integrated systems** theme. Architectures interaction with technologies are covered by the **Optimal architectures for complex algorithms implementations** theme, with a recent growing activity on reliability. Specific architectures for wireless digital communication are handled by the **Software defined radio** theme with internal collaboration with other teams of Telecom-ParisTech. The **Trusted computed hardware** theme, aiming at protection of hardware for security, has started seven years ago. It has now reached a mature status, with national and international recognition, as stated by the sabbatical of Pr Homma (Tohoku University), and the association of industrial researcher H. Chabanne from Sagem.

Team members, located in the sites of Paris and Sophia-Antipolis (LabSoC), have a strong teaching activity in the undergraduate and Master level at Telecom-ParisTech. Lectures are in the fields of digital electronic design, embedded systems design, SoC (Systems-on-Chip) design and embedded systems security. An important part is given to practical aspects which are covered by supervisions of numerous student projects. The latest research results feeds our teaching activity, for example, ANR SoCLib project methods are directly used in the “System-On-Chip Design” track of our Master of engineering. Team members have teaching activities and responsibilities at Master level in several others institutions such as Eurecom, Nice Sophia-Antipolis University or Paris-6 University.

2.2 Main Results

The main research results obtained during the period 2005-2009 are presented below for the research areas of the Complex Digital Electronic System team.

2.2.1 Design Space Exploration and Assisted Refinement of Integrated Systems

Faculty L. Aprville, R. Pacalet, S. Coudert, R. Boulifa

Main events Release of the TTool¹ Open Source toolkit, creation and animation of the SAFA (Sophia-Antipolis Formal Analysis Group) workshop.

Projects Industrial contracts with Texas Instruments (CASA I to V) and UDcast, Cifre PhD with Freescale, FP7 european project EVITA.

The increasing complexity of Systems-on-Chip requires new design and verification methodologies. The approach developed at LabSoC relies on modeling at a very high level and on early verifications in the design cycle. A strong separation between control and data processing is introduced. At the highest levels data processing is completely abstracted away. Control-oriented tasks exchange abstract and valueless samples, allowing ultra-fast simulations and static formal verification. This approach is supported by a dedicated UML profile (DIPLODOCUS) and a software toolkit (TTool). For the period 2005–2009 the main achievements are:

System-level Design Space Exploration (DSE): definition of a three-step methodology, comprising application modeling, architecture modeling and mapping of the former on the latter [228]. Simulation and formal verification are used in the first and second steps [194]. The current focus is on the post-mapping verification.

Fast simulation techniques: design and SystemC-Based implementation of a speculative, transaction-based simulation framework [232]. The current focus is on concurrent exploration of different simulation traces.

Formal verification: formal definition of the sets of primitives used in application and architecture modelling. Formal description of the mapping phase [194]. To overcome limitations of reachability analysis (e.g. combinatorial explosion, lack of refinement techniques), a formal verification scheme based on Description Logics has been investigated [195].

The TTool toolkit: design and open-source release of a toolkit. This toolkit shares several features with related works of the team, on distributed systems [161] [160] and requirement capture [194]. TTool is supported by Freescale. The whole framework is currently used in the context of the European project EVITA for security modelling and analysis on automotive security.

IP abstraction: proposal of abstraction techniques of fully detailed existing virtual components to allow high level verification. IPs at Register Transfer Level are raised at Bus Cycle Accurate Level [286] for verification. Data values are abstracted and replaced by data “presence” and dependency calculus replaces value computation. We then verify by model checking that outputs are produced at the expected dates with respect to the arrival dates of the corresponding inputs. This data dependency analysis also leads to control and data separation: [248] control or data labels are propagated from the primary inputs to the primary outputs and the module is sliced in two disjointed regions, allowing data abstraction and fast verification of the control slice. This work is theoretically founded. It relies on a rigorous approach of semantic data dependency at propositional logic level (and thus gate level). Approximations and complementary techniques (such as data bundling) are introduced to mitigate the complexity through accuracy-runtime trade-offs. This work also impacts our research activity on security.

¹<http://labsoc.comelec.enst.fr/turtle/ttool.html>

2.2.2 Trusted Computing Hardware

Faculty J-L Danger, T. Graba, S. Guilley, P. Hoogvorst, Y. Mathieu, R. Pacalet

Main events DPA Contest organisation at CHES'09.

Projects ACI MARS, ANR SAFE, ANR SeFPGA, ANR ICTER, System@tic Pôle "Secure Algorithm", TES Pôle "EPOMI" STMicroelectronics "PACA Lab" projects SecMat, SecBus, SecKer, CALMOS, Cifre PhD with STMicroelectronics.

Cryptoprocessor implementations can be attacked by taking advantage of physical emanations when the ciphering operations are executed or by injecting faults to modify its behaviour. The attack goal is to recover the secret key of the known algorithm, or to modify the code stored in external memories. The research challenge is to provide countermeasures able to thwart all kind of attacks. The passive attacks also called "Side Channel Attacks" are based on the analysis of the physical activity which can be made either by observing the power lines or the electromagnetic field radiation. The active attacks consist in injecting faults. They can be generated by methods like underpowering the power supply, parasitic glitches emissions or laser shots. The attacks and therefore their protections are done at circuit level or board level. At circuit level they aim at recovering the ciphering key or even the algorithm. They can be performed on any circuit from the smartcard to the big VLSI devices.

Board-level probing attacks use external memories and memory buses as natural targets. Adversaries can acquire confidential data and even corrupt the execution of critical programs by much simpler means than what is required for silicon-level attacks.

Protection at logical and physical level: These protections have been carried out on customized prototypes ASIC circuits and FPGAs in order to understand the attack mechanisms and devise efficient countermeasures. Four ASIC chips have been designed in 130nm STM technology. They embed a complete System-on-Chip including cryptoprocessors implementing DES and AES algorithms with different level of protections. Passive attacks [220] and active attacks [253] have been performed successfully on unprotected cryptoprocessors [165]. New attacks have been devised. Some of them take advantage of the Electromagnetic acquisition [177]. Countermeasures based on differential logic [278] have been evaluated and their efficiency has been demonstrated [164, 166]. Protections against attacks have been carried out on both embedded and commercial FPGAs. A custom fully reconfigurable embedded FPGA has been designed [205] to allow dynamic reconfiguration of the cryptoprocessor in case of attacks [206]. Asynchronous logic in a custom FPGA has been investigated as a countermeasure against passive attacks. The architecture has been studied at both the interconnexion level [209] and the programmable cell [263]. A versatile cell with multi-style capability has been studied in order to increase the robustness [199]. Many protections have been devised and evaluated in commercial FPGAs. They are based either on "Hiding" techniques [224, 221] by using differential Logic or "masking" techniques [226] by using a random variable. Their complexity and computation performances have been optimized [225]. Innovative structures to protect cryptoprocessors have been patented [276, 275, 279, 274]. Fault attacks have been studied by simple and efficient methods like underpowering the power supply on an AES cryptoprocessor [231]. The random number generation which is critical to obtain a good entropy has been studied with a high speed constraint. A novel open loop TRNG structure based on a latch chain has been devised and tested [162].

Protection at board level: In close partnership with the Advanced Systems Technology division of STMicroelectronics² we are working on a secure architecture [255] dedicated to medium to low end embedded systems and without any modification of the CPU nor the software design tools (which is considered unrealistic in this market). A strong cooperation between a trusted micro-kernel or hypervisor and a cryptographic peripheral (nicknamed SecBus) allows us to apply cryptographic functions only when needed and to select the less expensive among a set of candidate primitives for a given context. Our recent performance evaluations show that the SecBus

²Industrial contract PACA Lab 2004-2009, Cifre PhD of Lifeng SU

architecture is a very promising and cost effective solution to guarantee the nominal utilization of consumer equipments, and prevent the appearance of modchips or software jailbreaks.

2.2.3 Optimal Architectures for Complex Algorithms Implementations

Faculty J.L. Danger, P. Matherat, Y. Mathieu, L. Naviner, R. Pacalet, A. Polti

Main events NXP Semiconductor demonstrates Triscope project results at "Salon Européen de la Recherche & de l'Innovation". Invited conference at the Schloss Dagstuhl für Informatik GmbH.

Projects ANR SocLiB, ANR Asturias, ANR Telma, ANR Triscope, Bilateral Contract with STMicroelectronics for estimation of SER in Complex Systems, Bilateral contract with EDF for reliability improvement of systems implemented on FPGA

Architectures for image and multimedia processing: Video and image processing are more and more useful and required for numerous emerging services and applications but are often too computational expensive to reach real time on consumer devices (PCs, mobiles). On PCs platforms, we explored the usage a GPGPU (General Purpose Graphics Processing Unit) for a hardware accelerated multi-resolution and multi-prediction motion estimation algorithm used in real time video compression. This study led to very good performances [236] which are now confirmed by the success of the Compute Unified Device Architecture proposed by NVIDIA. Another weakness to note in the image processing on general purpose architectures is the inadequacy of cache memories architectures to the structured data organisation of multidimensional images. Furthermore, reconfigurable architectures (FPGA) designs are not well suited for complex cache management and leads to tedious developpement of ad-hoc memory management units. We proposed (ANR project Telma) a low-cost n-dimensional generic cache architecture for FPGA-Base image processing systems on chip exploiting spatial and temporal locality in a smarter manner than classical associative caches [234, 235]. Recent advances on high resolution LCD displays lead to emerging applications with 3D lenticular screens. In close partnership with NXP Semiconductor (granted through ANR project Triscope) we developed a real time hardware renderer for 3D LCD screens aimed to be the base for a full featured demonstrator for mobile 3D. For this purpose we designed a highly-integrated high-computational power embedded system (SHiX), featuring an embedded SuperH processor and a state of the art FPGA.

Architectures for communications: This activity is based on the global concept of reconfigurability. We defined a formal multilayer approach for reconfigurability in 3G systems. This formal framework was successfully applied to the DS-CDMA downlink detection giving a set of reconfigurable receivers appropriate for terminal implementations [168, 169, 268]. Time Interleaved High-Pass Sigma Delta converter (TIHP- $\Sigma\Delta$) assures the reconfigurability required by multistandard applications, but the digital processing inherent to this approach remains a bottleneck to achieve the ADC expected performances. We proposed a solution for signal reconstruction combining Comb-filter and decimation that reduces considerably computational requirements. Digital post-processing implementation based on our approach needs only a couple of integrators and differentiators [198].

Energy consumption of digital circuits and clockless systems: This research activity aims at modeling interactions between energy consumption of digital circuits, asynchronous circuits (with no global clock) and questions about algorithmic complexity. An initial work on dissipation of computation showed to us the links between language (computation) and matter (physical dissipation). This lead to the definition of "logical dissipation", and showed that a minimal dissipation is linked to modularity, on one hand modularity in space (composition of complex circuits from primitive gates), on the other hand modularity in time (questions about "synchronization"). A better practical knowledge of the dissipation question for the design of VLSIs could come from a better formalization of asynchronous circuits, seen as a general frame for defining digital electronic components [171].

Reliable architectures: The semiconductor scaling process is reaching some important limits that reflect negatively in the reliability of the integrated circuits. Some of these are manufacturing imprecision, improved susceptibility to environmental factors and physical parameters variability [172]. Our work deals with challenges related to such reliability decrease and has been developed in collaboration with the “Electronics and RF systems” team. During the period concerned by this report, we focused on development of efficient algorithms and tools for reliability assessment, which is crucial to establish cost-quality trade-offs related to different reliability improvement schemes [383].

Using a new 2×2 matrix signal representation, we proposed a method for reliability analysis based on the cumulative effect of errors in the signals of the circuit [163]. The proposed *Signal Probability Reliability* (SPR) model embeds the contribution of multiple simultaneous faults to the reliability of the circuit. We registered the developed software implementing the SPR algorithm [289]. We also developed a new approach to obtain the reliability information based on the circuit’s capacity of logical masking. This *Probabilistic Binomial Reliability* (PBR) method associates fault-injection and simulation to determinate an analytical equation for the reliability [211]. With the reliability equation available, many types of analysis can be done, like the susceptibility of the circuit to single and multiple faults, the reliability of the circuit for any particular value of individual gates reliability [210, 173].

Both proposed PBR and SPR approaches give accurate reliability values while requiring less computation power than state-of-the art methods. Furthermore, the proposed methods allow several trade-offs between accuracy and computation complexity for reliability assessment.

2.2.4 Software Defined Radio

Faculty J-L Danger, R. Pacalet

Projects IDROMEL(ANR) , Low cost UWB (Orange Labs contract), PFMM (French cluster SCS, DGE)

Flexible architecture for the Software Defined Radio (SDR)

Nowadays mobile communication systems, operate in different radio spectrum, radio access technologies, and protocol stacks depending on the network being utilized. This gives rise to the need of a flexible hardware platform that would be capable of supporting all the different standards in the entire wireless communication frequency range. This platform shall of course be extremely power efficient.

In a large multi-projects³ context we propose a generic baseband prototype architecture for SDR applications[250], subdivided into a high level control module and a digital signal processing engine. This architecture can be used, for instance, in cognitive radio contexts[227]. The DSP engine is a composition of highly configurable processing blocks, like a “Fourier transform/vector processing” block [243, 242] or a generic channel decoder [188, 187, 243, 251], each dedicated to specific algorithms based on the analysis of different standards. Most existing works in the field are based on specialized micro-processors (vector processors, VLIW, ASIP, etc.) and on advanced interconnects (Networks on Chip). Unfortunately these solutions are still usually above the maximum power budget for such applications. Our approach mainly consists in identifying a small set of very complex hardwired processing blocks that will take in charge 90 to 95% of the total baseband processing power in a very power-efficient way. Each block is highly parametrizable and is assisted by a minimal 8 bits micro-controller that allows it to run sequences of operations (e.g. channel estimation) from basic commands (Fourier transforms, component-wise products, etc.) The platform is open and the whole project will be distributed under the French equivalent of the GPL-LGPL open source licenses, both for hardware models and embedded software.

Low cost UWB receiver

In order to meet the low-cost constraints and be Software Defined Radio compliant, the studied Ultra Wide Band communication systems is based on “Impulse-radio” protocols. The analog

³IDROMel ANR/RNRT, PFMM (French cluster SCS, DGE), OpenAirInterface

part is made up of an antenna a Low noise amplifier and an energy detector which allows to meet the low cost requirement. The research challenge is to study complex algorithms to check the possibility to obtain high bit rate (around 100Mbps) even with a simple radio front end. In the frame of a collaboration with Orange Labs, the studies lead to three main results.

The first one is the probabilistic equalizer which is based on accurate energetic channel model. This function runs jointly with an iterative channel decoder to improve the bit error rate [241].

The second result is the development of a new algorithm to estimate the energetic coefficients. This function takes advantage of the Expectation-Maximization algorithm (EM) to provide accurate energetic coefficients to the equalizer [240]. A simpler method based on a novel training sequence has been proposed [277]. The final result is an optimal architecture in a fixed point precision. The optimisation is partly based on a chi-square law approximation with a gaussian distribution. This implementation allows to meet the low-cost constraint of the digital implementation [239, 238].

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Chapter 3

Electronics and RF Systems (ELECRF)

Team leaders B. Huyart (P), P.Loumeau (P).

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F. Ziade (06/04–04/08),	A. Latiri (07/04–06/08),	M. Vahdani (10/04–10/08),
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K. Mabrouk (12/05–12/08),	M. Bahouche (02/06–),	H. Khushk (09/06–),
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Faculty IT	9.5
PhD students	13.8
Post-docs, engineers and sabbaticals	2.5
Defended PhD theses	19
Defended HDR	2
Journal papers [published, in press]	[23, 2]
Papers in conference proceedings	125
Chapters and books	3
Patents and software	[8, 1]
Grants [public, private, european] (k€)	[799, 420, 38]

3.1 Objectives

Our research deals with the integration of radiofrequencies devices for wireless mobile communication systems. The activity is supported by public and industrial fundings. The current developments are made in close interaction with companies like STMicroelectronics, NXP, CEA, Thales, Thales Alenia Space, Thales Airborne Systems, LNE, Orange Labs or Schneider Electric.

From reconfigurable RF front-end to software defined radio: The goal of the software defined radio is to shift the processing of the received signal into the digital domain with the analog to digital conversion immediately after the antenna. The “software defined radio” main interest is to facilitate standard reconfigurability using numerical processing and simple software downloading. Yet, the software defined radio calls for analog to digital conversion performances that are not currently achievable. Our work concentrates on the joint optimisation of analog and digital functions that would match the software radio feasibility criteria. Novel concepts of electronics architectures are demonstrated through the design, fabrication and test of innovative CMOS circuitries. The long term goal is the building of a software defined radio platform.

Nanoelectronics architectures and circuits: New applications require an increased level of hardware and software integration at chip level if one wants to keep a moderate manufacturing cost. The evolution towards nanoscale technologies for a higher transistor density is therefore essential in a highly competitive environment. However, at decananometric or nanometric scale, new physical phenomena must be taken into account and modeled. Our research covers the modeling and the evaluation of new nanodevices as well as the study of smart acquisition interfaces satisfying the requirements of performance and portability.

RF metrology: Increasing communication rates as well as more demanding requests on wireless systems calls for improved RF metrologies. Our work involves:

- The new definition of standards for the measurements of both RF power and RF scattering parameters in the frequency range 1-18 GHz.
- The non-linear characterisation of power amplifiers in the frequency band 900 MHz - 10 GHz using numerical pre-distortion and source&load pull techniques.
- The sounding of indoor radio propagation channel in the ISM band 2.4 GHz.

Wireless communication systems technology: Our work on wireless communication systems technology concentrates on the so-called “RF front-end” which is one of the most sensitive part of communicating objects. It involves improved modulation/demodulations schemes as well as the study of novel antenna technologies applied to several wireless communication standards from 800 MHz to 40 GHz. The design of circuits for RF “Front-end” using MMIC or hybrid technology on GaAs or dielectric substrates is based on the three-phase “Zero IF” receiver. Concerning

the antenna design, the demand is currently on wideband and discrete structures in many application areas. Our research topics are focused on wideband and low-profile antennas and arrays. In the last few years, we developed and applied novel artificial materials for the antenna's reflector which has led to innovative and performant antennas. These materials are also used to reduce the coupling factor between the elements of an antenna array.

3.2 Main Results

The main research results obtained during the period 2005-2009 are presented below for the research areas of the "Electronics and RF systems" team.

3.2.1 From Reconfigurable RF Front-End to Software Defined Radio

Faculty P.Desgreys, V.T.Nguyen, H.Petit, J.F.Naviner, P.Loumeau

Main events Technical chairman of NEWCAS-TAISA09 IEEE International Conference, Coordinator of the project TEROPP between 6 Carnot Institutes and 3 Fraunhofer institutes, creation of the GIS eSys "Groupement pour l'Electronique des Systèmes".

Projects Versanum ANR-05-RNRT-010-01, TEROPP ANR-07-P2IC-O11 01, HyperSCAN ANR-06-TCOM-023-06.

Direct RF sampling and signal processing: Direct analog to digital conversion of the radio frequency (RF) signal is still unfeasible at present time, due to the high requirements imposed on the analog to digital converter. This motivates the need for a highly flexible RF front-end that can be fully integrated in low cost digital deep-submicron CMOS processes. Different techniques for shifting the RF and analog circuit design complexity to a digitally intensive domain were developed recently. A collaborative project with STMicroelectronics [299] was launched on direct RF sampling and discrete-time analog signal processing. The goal was greater flexibility and reduction of cost and power consumption in a reconfigurable design environment. To validate the flexibility and reconfigurability of the receiver, GSM and 802.11g communication standards have been addressed and adopted during system level study. The frequency plan and filtering scheme were made different for each standard to fully analyze and validate the flexibility of the architecture. A circuit designed and fabricated in 90nm CMOS technology was able to demonstrate the functionality of the receiver.

Non-uniform sampling: In the previous project, the first CMOS anti-alias filtering stage was improved but the costly discrete RF filter could not be suppressed. To deal with the suppression of RF anti-alias filtering, Non Uniform Sampling (NUS) based receiver architectures have been studied in a collaborative project with SUP'COM Tunis. We have demonstrated relaxed constraints on both RF filter and ADC dynamic power consumption using appropriate NUS architecture [307].

Analog to Digital Phase Locked Loop (ADPLL) : An RF synthesizer is another challenging block of a RF analog front-end. By replacing analog blocks, it allows simpler reconfigurability in the case of wideband and high resolution applications. In this context, an ADPLL architecture has been studied and modeled in order to minimize the jitter noise and facilitate the design reuse [290]. The architecture was then proposed to NXP who has fabricated a successful demonstration circuit in 65nm CMOS technology in a collaborative project with TELECOM ParisTech. The collaboration will continue with a new CIFRE thesis.

Software defined radio: The development of the software radio concept is still very much limited by the available resolution and speed of the analog-to-digital conversion stage. Parallel analog-to-digital converters (ADCs) seem to be the best suited way of increasing analog-to-digital conversion rates in complementary metal-oxide-semiconductor (CMOS) technologies. To demonstrate the feasibility of such a goal, a four-channel time-interleaved (TI) SD ADC has been implemented in an advanced 65 nm CMOS process [198]. The objective was an EDGE/UMTS/WLAN

tri-mode TI ADC with signal bandwidth from 135 KHz to 12,5 MHz and resolution from 8 bits to 13 bits. Three intrinsic problems must be overcome: gain-mismatch, offset-mismatch and clock-skew. Among these problems, clock-skew is the more challenging one. We have implemented and demonstrated experimentally a new Mixed-Signal Clock-Skew Calibration Technique based on a digitally trimmable multiphase sampling clock generator. This demonstrator can correct an initial clock skew of thousands of picoseconds with a granularity of 1.8 ps [294]. Three patents have been filed like [440], [448].

Opportunistic Radio: Cognitive radio systems are aiming at seamless mobile connectivity and optimum spectrum management. The ultimate evolution is the *opportunistic radio* where the intelligence and decision power are mostly on the terminal side. Ideally, a wide band RF front-end for future mobile opportunistic terminals must cover multiple standards and bands (from 400 MHz to 5 GHz), and is able to scan the spectrum to detect un-used bands. In this context the team coordinates the TEROPP project - Technologies for terminals in opportunistic radio applications - that associates six Carnot Institutes and three Fraunhofer Institutes. The team is implied in the co design and optimization of the wide band front end elements. This project started in January 2008 and the end will be in September 2011.

Disruptive technologies Disruptive technologies offer new ways to accomplish breakthroughs in cognitive and opportunistic radio. Based on superconductivity physics, the RSFQ (Rapid Single Flux Quantum) logic is a very low power consumption and ultra-fast electronic logic which is considered as the best alternative to CMOS in the ITRS for ultra high frequency applications. The team has proposed an RFSQ Analog to Digital architecture in a work conducted within the ANR Hyperscan project. The goal is a 2x8 bits and 500 MHz BW Sigma-Delta analog-to-digital converter (ADC or CAN) circuit with performance specifications to achieve space telecoms at 30 GHz carrier frequency. To verify the project feasibility, we have developed a model that implements superconductivity physics into the RFSQ ADC circuit simulation [433].

3.2.2 Nanoelectronics Architectures and Circuits

Faculty P.Desgreys, J.F.Naviner

Projects NANOSYS "Action Concertée Incitative CNRS", NANO-RF (Institute Telecom incentive project), French-Brazilian project supported by CAPES and COFECUB.

Currently developed technologies in electronics have all entered the nanoscale area, and low dimension physical phenomena which need not be taken into account in the past cannot be anymore neglected in the design process. Disruptive technologies are emerging and may become alternatives or complements to the massively used CMOS technologies in the future [172].

In 2004, we began to study novel nanoscale technologies in order on one hand to propose new methods in the architecture and circuit design process and on the other hand to evaluate potentialities of emerging technologies compared to MOS technology. More precisely, the following axis were considered:

- portability of mixed-signal architectures to CMOS nanoscale technologies (collaboration with STMicroelectronics);
- reliability of mixed-signal architectures and circuits;
- device modelling, performance assessment and the comparison with MOS.

Note that the last two axis were initiated in the frame of the national Action Concertée Incitative NANOSYS whose objective was "Architectures pour l'intégration des nanocomposants moléculaires".

Device modelling, performance assessment and comparison with MOS: Both fundamental and economical CMOS limits generate the need for complementary and alternative technologies, with molecular electronics among the most promising ones. In the framework of two projects NANOSYS (ACI) and NANO-RF (Institute Telecom incentive project) in 2006 and 2007, a compact model of Carbon Nanotubes Field-Effect Transistors (CNTFET) was developed in VHDL-AMS language [336] to explore the high-frequency performance of CNTFET. We have shown

that the cut-off frequency expected for a MOSFET-like CNTFET is well below the performance limit, due to the large parasitic capacitance between electrodes. Our model demonstrates that an array of parallel nanotubes combined in finger geometry to produce a single transistor channel significantly reduces the parasitic capacitance per tube and, thereby, improves the high-frequency performance.

Architectures for sensor devices: Supported by a French Brazilian CAPES/COFECUB project, we have developed research activities on the reconfigurability of sensor acquisition interfaces. Reconfigurability is needed to adapt the interface characteristics to those of a particular sensor, to those of the environment and to the particular type of measurement. These objectives are in some way a generalization of the calibration or self-calibration problems.

Our main target was the biomedical area where each patient's impedance is different. Yet, the measurement accuracy must be kept unchanged to allow for rigorous diagnosis and appropriate medical decisions. Our main result was the development of an automatic compensation method that deals with the impedance mismatch of electrodes in applications like ECG, EEG, etc. This thematic has now been integrated in the researches on reliability considering the convergent problematics [379], [380].

Reliability of mixed-signal architectures and circuits: Works on reliability of analog or mixed-signal architectures were initiated in Oct. 2008 with a PhD thesis. Considering that many circuits are today Systems-on-Chip (SoC), that they include often various analog or mixed-signal sub-circuits and that the reliability level of a SoC results from both the reliability of each sub-parts and the connections/interactions between them, our main objectives are:

- to assess the reliability of basic functions considering the physical causes of failures (ageing effects or other causes),
- to assess the reliability of an architecture working at an abstracted behavioural level,
- to compare basic functions circuitries and architectures on both performance and reliability criteria,
- to define methods of architecture/circuit design that includes the reliability in the design criteria.

The present work is focusing on the reliability of radio-frequency front-ends. After studying the causes of degradation and failures in nanoscale integrated circuits with ageing, we have been working on the reliability assesment of a Low-Noise Amplifier (LNA) given the reliability model of the constituting devices [362].

3.2.3 RF Metrology

Faculty X.Begaud, E.Bergeault, J.C. Cousin, B.Huyart

Main Events Organization of European Microwave Week in Paris on October 2005, Invited seminar on Radar Systems at Universidade Federal do Rio Grande de Norte Natal Brazil, October 2008.

Projects Bilateral project with LNE, Bilateral project with Orange Labs, Bilateral project with Schneider Electric, French-Brazilian project supported by CAPES and COFECUB, ANR Smartvision (Système multi senseur de détection d'objets cachés).

Power Standards: In the HF domain, the power standard is made of a calorimeter including a bolometric fixture. A power standard has been designed and fabricated in coplanar (CPW) waveguide technology with low return losses up to 8 GHz. A 3D electromagnetic simulation and measurements using TRL (Thru, Reflect, Line) calibration were performed. It allowed us to set the entire electrical model, including :

- the transition from the SMA connector to the CPW line
- the radiation
- the γ constant of propagation and the Z_c characteristic impedance of the CPW line

- the DC-blocks

The obtained average deviation between the computed and measured efficiency by a micro calorimeter is less than 1.2%. The computed and the measured values are sufficiently close to open the way for a programmable HF power standard using our technology. [394], [395].

Standard Impedances for S-parameters measurements by a probe station: In the context of the design of Microwave Monolithic Integrated Circuits (MMIC), the devices characterisations should be performed on wafer for optimising the reliability and reducing the manufacturing cost. Valid S-parameters measurements with a probe station is however an open metrological challenge. In collaboration with the LNE institute, we realised and tested calibration kits associated to a TRA (Thru, Reflect, Attenuator) calibration method for probe station measurements [398, 395]. The main advantage of the TRA method in comparison with usual LRL (Line Reflect Line) method is the small size of the new standards. In that way, the manufacturer may add standards on the tested devices wafer to get the same electromagnetic propagation conditions between the calibration step and the test step.

Non-linear characterisation of power amplifier: Non linearities at the RF front-end level (power amplifiers for example) are a known source of impairment in wireless digital communications. It causes spectral spreading over adjacent channels and distorts the base band data. To reduce the distortion effects due to power amplifiers in the case of FSK, QPSK or 16 QAM modulated signals, we have successfully exploited a linearisation technique using a memoryless computed predistortion of the base band signals [314]. In order to study the impact of source and load impedances at the fundamental, the second harmonic and the low (base band) frequencies on the linearity, the efficiency and the level of transistors output power, a “sourcepull”-“load-pull” characterisation bench involving base band predistortion has been demonstrated [292, 315, 326, 293].

Radio communications channel sounding: The performances of a wireless propagation channel are strongly dependent on the propagation environment between the emitter and the receiver. A real time propagation channel sounder operating both in the time domain and the spatial domain is highly desirable for the complete characterisation of the channel specificities. Most sounders consist of virtual antenna array or actual antenna array with switches and few emitting elements. Furthermore, the output of the antenna is generally connected to a Vector Network Analyzer (VNA).

In contrast, we developed a sounder designed for non stationary MIMO channel. The sounder is formed by a linear array of 4 antennas at the emitter side and a linear array of 16 antennas at the receiver side. Each antenna is connected to a low cost zero IF receiver designed by our lab (see next section). The number of emitting elements permits to increase the range of Doppler frequency measurement and the precision on the AOA of the waves.

The current version of our sounder measures the angles of arrival (AOA) of impinging waves in the azimuth and elevation plans altogether with their time delays in indoor environments. In the case of non stationary channels, a chirp signal (FMCW) is used instead of step CW in order to reduce the acquisition time of the sounder [303]. To demonstrate the unique accuracy of the sounder in time-varying environments, we carried out an experimentation with an emitter moving at a speed of 1.1m/s along a rail parallel to the receiver antenna array. The Doppler frequency was measured with a precision of 0.2 Hzs [363].

The angles of departure of the emitted waves are determined using a virtual array of a 4 antennas. In that case, the channel is assumed to be stationary [364]. The sounder operates in the frequency range 2.1-2.8 GHz which is the bandwidth of the quasi Yagi antenna we have designed. The time and angle resolutions of the sounder are respectively 0.5ns and 2° using sub space methods (MUSIC, ESPRIT).

The correlation between the orthogonally polarized waves must also be characterized if the designer wants to exploit polarisation diversity. The quasi Yagi antennas have been replaced by a home designed array of 4 double patch antennas which covers up to 500 MHz around 2.45 GHz with double polarisation capability. A 2-paths scenario was emulated in an anechoic chamber and joint time delay, direction of arrival and polarization estimation were performed. The discrepancies between theoretical and experimental values are less than 0.2 ns and 10° [365].

A second research activity concerns the design of a channel sounder for Ultra Wide Band (UWB) applications. A UWB RF receiver using microstrip technology on FR4 substrate and UWB antennas have been codesigned in the frequency range 6-8.5-GHz [338]. Presently, the SIMO channel sounder consists of 8 receivers. Preliminary experimental results performed in an anechoic chamber demonstrate an accuracy of 2° for the AOA (Angle of Arrival) azimuth and 0.1 ns for the delay [339].

A third research activity was the realisation of a MIMO platform which was derived from the sounder by replacing the FMCW source by 2 modulated sources of same frequency 2.4 GHz. Beamforming was used in order to recover the signal of each source and improve the link quality in a spatial multiplexing mimo system [319].

Radar Applications: The aim was the design of a short range (1 m) radar operating in the 2.45 GHz ISM band for low cost operation. The challenges for such radar are the detection of close targets using a signal of limited frequency bandwidth (80 MHz) and the rejection of unwanted signals in a widely used electromagnetic band. Using our work on demodulation techniques, a new radar architecture was designed based on a coded BPSK signal and the phase difference detection of successive carrying signals) [367]. This work has been done in collaboration with Schneider Electric and protected by two patents [442].

3.2.4 Wireless Communication Systems Technology

Faculty X.Begaud, B. Huyart, A.C.Lepage

Main events PhD Award of the Thales AirBorne Systems (2006, L. Schreider); UWB Autumn's School: Communications, Localization and Radar (23th-27th October 2006, Valence, France) in the framework of GDR ONDES, CNRS with X.Begaud (General Chairman).

Projects CONRAHD/OPTIMUM (CONnexion Radio sans fil Haut Débit) from the French Cluster "SYSTEM@TIC PARIS-REGION", bilateral projects with Thales Airborne Systems (2) and Thales Air Systems (1); PUMA (Produit Ultra haut débit sur bande millimétrique) from the French Cluster "SYSTEM@TIC PARIS-REGION"

MMIC design for RF "front-end":

Within the framework of CONRAHD/OPTIMUM, we realised the design and the tests of an integrated mixer circuit on GaAs in the 40.5-43.5 GHz frequency bandwidth. This resistive up-converter mixer is dedicated to Local Multipoint Distribution Service (LMDS) applications for short ranges, multi users, multi applications or multi communication standards. The mixer is the most linear device reported so far in this bandwidth in the up-converter mode [356].

For software defined radio applications, we have designed a Zero-IF three-phase demodulator in MMIC technology operating in the 1-24 GHz bandwidth to solve the problems of multi standards management [414, 443]. The main contributions were in the design of phase shifters and distributed mixers which can operate in such very large bandwidth.

Another three-phase demodulator and a classical IQ demodulator using Gilbert cell have also been designed and compared around 40 GHz [439]. They demonstrated direct demodulation of high frequencies signals (upper than 40 GHz) to base band signals. We proved that the use of Zero-If three-phase demodulator cancelled naturally the DC offsets as well as many damaging non linearities effects for the reception sensibility of a Zero-If demodulator [441]. An original calibration algorithm, based on blind technique using an unknown signal for homodyne receivers, was developed to reduce significantly the physical and signal processing stresses in the case of software radio applications [302]. The results were further improved using a bytes synchronisation method for blind-calibrating the demodulator and taking into account the defaults introduced by the propagation channel [360].

Wide band antennas design: The RF team's research is devoted to the design of wideband antennas and arrays. We initially focused our work on UWB (UltraWideBand) applications. While most UWB studies concentrate on omnidirectional antennas, we developed a unique compact,

directive UWB antenna with excellent performance both in frequency-domain and time-domain between 3.1 and 6 GHz [301]. The directive UWB antenna enables a 6 dB improvement in the budget link, a key feature in UWB considering the low power level of the emitted signal.

Following these results, we concentrated on the design of novel artificial materials to reduce the thickness of wideband antennas. The developed materials exploit periodic structures in order to exhibit the behaviour of an Artificial Magnetic Conductor (AMC) as well as that of an electromagnetic band-gap (EBG) structure. We designed a novel reflector that could demonstrate a wideband AMC behaviour (no phase shift on the reflected electric field) over a decade. By placing a radiating element very close to this reflector, we conceived the world's first antenna which is able to work over a decade with a thickness of one hundredth of the wavelength of the lowest frequency. This work has led to a patent [450] and has received the Thales Aerospace Division's PhD Award in 2006.

Our current challenge is to improve the radiation efficiency and the gain of these antennas. We are developing a new methodology that takes into account the interaction between the radiating element and its artificial ground plane. We are also considering refined characterizations of the artificial material itself, a necessary step for improving our control on the phenomena occurring in these metamaterials [343, 384]. Finally, we are applying our artificial materials to the problem of reducing the coupling between elements in a wideband antennas array, with a focus on analytical models [358].

Most wideband antennas, eg. spiral or sinuous antennas, require a balanced feeder which is generally bulky. Recently, we demonstrated a compact wideband coplanar balun for dual polarized compact antennas. We proved in this work that it is possible to design a small size feeder closed with a radiating surface [451].

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3.3.1 ACL: Articles in ISI-Indexed Journals

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Chapter 4

Optical Telecommunications Group (GTO)

Team leader Didier Erasme (P).

Faculty

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PhD students

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F. Kéfélian (10/02–12/05),	B. Bristiel (11/02–03/06),	J. Renaudier (11/02–05/06),
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M. Sabban (11/05–04/09),	D. Fafchamps (10/05–),	J. Petit (10/05–),
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S. Hocquet (10/06–),	B. Bennai (11/06–),	S. Cordette (12/06–),
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S. Mumtaz (10/07–),	J.C. Antona (12/07–),	M. Selmi (10/08–),
G. de Valicourt (10/08–),	H. Brahmi (12/08–).	

Post-docs, engineers and sabbaticals

M. Costa E Silva (03/05–01/09),
C. Gosset (09/08–08/09),
F. Mendieta (Prof. Sabbatical, CICESE) (08/05–03/08).

External collaborators

J-C. Bouley (prof. associé) (07/04–).

Faculty IT	6
PhD students	13.8
Post-docs, engineers and sabbaticals	1.5
Defended PhD theses	22
Journal papers [published, in press]	[65, 8]
Papers in conference proceedings	101
Chapters and books	1
Patents	2
Grants [public, private, european] (k€)	[808, 512, 120]

4.1 Objectives

The evolution of optical communication systems represents a particularly challenging guideline for research activities taking place in the GTO group of Télécom ParisTech. In addition to the topics directly relevant to the upgrading of optical networking techniques, architectures, devices, components, etc., our field of activity expands both to subjects that use similar methods and technologies and to characterization systems for telecommunication devices.

The last four years have represented an important transition period for the field of optical communications. In the early 2000s, the strong development of the “copper” access techniques (ADSL, Cable) and the remaining overcapacity of metro and core optical network opened a new development window for the next technology step strongly relying on optical technology: The transition of the fixed access network onto an optical medium (FTTH), convergence techniques for carrying mobile communication signals over optical fibre (RoF), access-metro convergence and finally the new requirement for capacity and connectivity in the metro and core networks due to the explosion of the data exchanges for domestic (HDTV, VoD, P2P) and professional (data storage) applications. Presently, the three traditional fixed-network segments are all going through a strong evolution process:

- with the deployment of FTTH (Fibre-to-the-home) the optical access network has had to enter the industrial world with some rather traditional solution while more advanced solutions which would better take advantage of optical technology are still under strong competitive development (WDM PON, OCDMA, ...)
- the metropolitan network has to cope with an enhanced connectivity and some constraints related to equipment cost which requires new technical solutions;
- in order to cope with the increase in the capacity demand, the core network is migrating toward higher individual channel bit-rates up to 40 Gbit/s (leading to new transmission impairments) in a rather traditional way. However, it is now quite clear that further evolution, which requires a better usage of the fiber optics bandwidth, has to rely on new paradigms. These are bound to make use of the somehow under-considered knowledge in digital communication that has been the key development tool of RF mobile communication.

In this wide panorama, the GTO group relies on its theoretical competence and its modeling and experimental know-how for developing new concepts and for participating in advanced collaborative research on optical systems.

A first research axis concerns the development of new optical signal processing techniques and functional analysis of new components for communication systems. This field covers several

related studies gravitating around non-linear optical effects and behaviors concerning light emission, light amplification, light transmission and new reception techniques. Although a number of applications lie in the field of telecommunication, the activity expands to other application areas such as energy, industrial process and defense.

The second research axis lies closer to the actual networks and transmission systems structures, approaching multiplexing techniques, high bit rate communication, new optical network architectural topics.

Finally, a specific research axis is related to quantum communications in particular to quantum key distribution for cryptography systems.

These research axes usually associate theoretical investigation and modelling activities, simulation using internal or commercial software and experimental activities. The latter rely on a rather well-equipped optical laboratory which includes a 10Gbit/s transmission platform, picosecond optical facilities allowing some 40Gbit/s experiments and dedicated characterisation and sensor set-ups.

The reporting period has been characterized by many national initiative interactions, (ANR, Cifre) and an enhanced activity on the European landscape. Through FP6-e-Photon/ONE⁺ and FP7-BONE and FP7-EURO-FOS Networks of Excellence our international activity has been pushed to a high level. In addition to project review papers involving many teams, we can count journal publications with laboratories representing as many as 7 different countries (Denmark, Japan, USA, UK, Mexico, China, Greece).

4.2 Main Results

The main research results obtained during the period 2005-2009 are presented below for the research areas of the GTO team.

4.2.1 Optical Functionalities and Novel Devices for Communication Systems and Networks

Faculty G. Debarge, D. Erasme, R. Gabet, P. Gallion, Y. Jaouën, C. Ware.

Projects RNRT-ROTOR (01/04–12/07), Pôle System@tic-CARRIOCAS (10/06–09/09), ANR AROME (01/07–12/09), ANR L2CP, FP7-EUROFOS (05/08–04/12), trilateral projet with EDF and LCPC, Bilateral project with ONERA and with CEA, 2 cifre PhD.

The widespread introduction of broadband at all levels of communication networks, the ubiquity of data exchange, the wired network infrastructure increasingly using the optical medium, and its being extended over the last mile all the way to the end-users, are changing the deal on signal processing functions implemented directly in the optical domain, giving them a foremost place in system design. These *optical functionalities* aim at keeping, as best can be done, the optical signal's integrity, avoiding optical-to-electrical conversions. The intrinsically high speed of the physical phenomena to be used allows them to take over processes which were traditionally implemented in the electrical domain, and the development of devices adapted to these applications.

Clock Recovery

Clock recovery of a high-bit-rate digital signal (10, 40, 160 Gbit/s) is a required functionality at transmission end or in a routing node. Two techniques have been studied: self-pulsating lasers and opto-electronic phase-locked loops.

On the former, the ROTOR RNRT project¹ to point out the interest in quantum dot lasers structures for optical engineering and radio frequency signals in general has demonstrated ultra

¹Partners: CNRS LPN Laboratory, Alcatel Thales III-V Lab, Alcatel Lucent, ENSSAT, EUROPTST, Highwave

low self-pulsating line width and the superiority of all-optical techniques over the electrical ones for clock recovery at 43 Gbit/s [638, 499, 500].

Second, a phase locked-loop can use a nonlinear optical device (SOA or PPLN) as an ultra-fast phase comparator. It allows clock recovery of RZ signals—and NRZ in some cases—as well as OTDM demultiplexing by recovering the “sub-clocks”. Collaborating with the Technical University of Denmark and the National Institute for Materials Sciences of Japan, we demonstrated sub-clock recovery [513] and full 1/64 OTDM demultiplexing at 640 Gbit/s. [469]. This was the second-ever demonstration of clock recovery at that high a bit rate, and the first involving a PPLN device, which was announced among record-setting postdeadline papers in OFC'2008.

This activity, in the framework of e-Photon/ONe⁺ and now EURO-FOS², was rewarded by an invited paper in the Journal of Lightwave Technology [490] and the “Letter of the Month” of Electronics Letters. It is now part of the topic of a franco-german PICF grant application and a Joint Experimental Activity in EURO-FOS.

New Semiconductor Sources and Devices

The recent evolution of the optical communication network led to a large demand for new low-cost and high-performance components. The CARRIOCAS project³ is dedicated to setting up an experimental 40Gbit/s network for high capacity data exchanges, included a task related to the development of low cost front end optical sources. In collaboration with GIE Alcatel-Thales III-V lab, we were able to demonstrate how “dual modulation”, consisting in modulating simultaneously the laser and the modulator of an EML source, leads to an extended transmission span (from 80 to 160 km) at 10Gbit/s [547]. Our involvement in the ANR project AROME⁴ is dedicated to the evaluation of the very large spectral bandwidth semiconductor optical amplifiers fabricated in GIE Alcatel-Thales III-V lab. The group is involved in other devices development through 2 new Cifre theses with III-V lab (on 100G receivers and reflective SOA modules for access networks).

Distributed Amplification

The distributed amplification based on the Raman effect, appears as an alternative or an additional technique to the doped fiber amplifier for optical telecommunications. However noise transfer from the pump noise to the signal, the pump polarization fluctuation and the double Rayleigh scattering strongly impact the noise figure [455, 479, 521, 539] and constrain their use. A new mechanism of noise associated with the fluctuation of the pump polarization induced by spatial fluctuation of the birefringence has been identified and allows interpretation of experimental reports [477]. As for the Brillouin effect, when it does not limit the power injected into a fiber, it is an irreplaceable tool in the design of optical sensors [522].

Brillouin Effect Applications

Given its low required power threshold, the Brillouin effect in optical fiber is one of the most promising nonlinear effect to design new all-optical processing. A self-referenced technique for measuring the Brillouin gain in an optical fiber has been recently proposed; and the importance of acousto-optic effective area in place of optical effective area on the Brillouin efficiency has been

²Partners: Institute of Communication & Computer Systems/ National Technical University of Athens (leader), Heinrich-Hertz Institute, University of Essex, Universitat Politecnica de Catalunya, ACREO AB, Technical University of Eindhoven, Research and Educational Laboratory in Information Technology, Chalmers University of Technology, University of Karlsruhe, Politecnico di Torino, University College Cork, Scuola Superiore Sant'Anna, Universidad Polytechnica de Valencia, Interuniversitair Micro-Elektronica Centrum IMEC, Instituto de Telecomunicacoes, Technical University of Denmark

³Partners: Bull, CGG-Veritas, Draka Comteq, EDF, France Telecom, GIE Alcatel Thales III-V lab, Hewlett Packard, Renault, Kylia, Medit, N2Nsoft, Oxalya, CEA, CNRS, Ecole centrale de Paris, IEF (université Paris-Sud 11), INRIA, Marben products, Prism (Université de Versailles Saint-Quentin), Supélec, Telecom et Management SudParis

⁴Partners: Alcatel Thales III-V Lab (leader), FTR&D, ENIB, INSA Toulouse, IEMN

confirmed for the first time [484]. The slow-light concept has been changed from a scientific curiosity to a rapidly growing field with many potential applications. We have demonstrated simultaneous demodulation and slow-light delay of DPSK signals at flexible bit rates using Brillouin based optical filtering effect. A record delay-time of 81.5ps with error-free operation ($BER < 10^{-9}$) has been obtained for 10Gb/s [518]. The delay and BER performance of 10Gb/s signal in Brillouin-based slow-light delay line have been evaluated in terms of NRZ, PSBT and DPSK modulations formats [517]. The distributed Brillouin-based optical sensors appear to be one of the most promising techniques for temperature and strain measurement. Under a collaboration with EDF, our contribution is focused on fiber design, especially in terms of Brillouin spectrum [541].

High-Power Fiber Lasers

The technology of rare-earths doped optical fibers (Ytterbium (Yb^{3+}) for amplification at $1\mu\text{m}$ and Erbium/Ytterbium (Er^{3+}/Yb^{3+}) at $1.55\mu\text{m}$) represents a strong contender for applications requiring high optical power. Our contribution is carried out mainly through collaborations with external laboratories and companies: ONERA, Keopsys and CEA [475, 537], PhLAM laboratory of University of Lille. With ONERA we have participated in the design of LMA fiber amplifiers [458, 460], in the analysis of Brillouin spectrum of doped fibers in connection with doping [459], and more recently the combination of coherent fiber amplifiers [481, 565]. The collaboration with PhLAM concerns the design of Ytterbium-doped solid core photonic bandgap fiber laser operating around 980nm [495].

Optical Low Coherence Reflectometry

The optical low-coherence reflectometer (OLCR) developed in our laboratory has been upgraded over the years and has proven to be a unique investigation tool for the study and the characterization of new photonic components. Through collaboration with component makers, we have been able to measure some otherwise inaccessible parameters in optical wavelength mux-demux, speciality optical fibers, fiber Bragg gratings, semiconductor devices, including semiconductor optical amplifiers... Our state-of-the-art phase-sensitive OLCR spatially resolves internal reflections of the device under test, and allows measurement of different polynomial terms of dispersion, birefringence, loss / gain material coefficient, phase/amplitude coupling coefficients. It has attracted many national and international collaborations, industrial and academic. The latest results concern the study of photonic bandgap semiconductor waveguides under the ANR-L2CP⁵ project [461, 492, 536, 546] in collaboration with Thalès R&T and specialty fibers characterization [467, 474] for which the OLCR provides incomparable elements of analysis.

4.2.2 Optical Network Evolutions

Faculty D. Erasme, P. Gallion, Y. Jaouën, C. Ware.

Projects ANR-Supercode (10/06–11/09), ANR-TCHATER (10/07–12/10), FP6-e-Photon/ONe⁺, FP7-BONE (1/08–12/10), FP7-EUROFOS (05/08–04/12), research project Orange labs, 3 Cifre PhD.

Fast development of bandwidth-consuming services like high-definition/on-demand television, network gaming, grid computing, makes stringent the need to further network capacity. The objective to provide broadband to a maximum of users ("broadband-for-all") has been leading research and development in the field of fibre-to-the-home (FTTH) technology for the deployment of high bit-rate access networks. The optical networks must manage interfaces with the copper links and radio access technologies (fixed or mobile) or eventually replace other solutions to provide unmatched performances. Deployment of FTTH will significantly impact the capacity requirement

⁵Partners: Thales R&T (leader), CNRS/LPN, IEMN

carried by metro and core transport networks in a medium-term future. The fiber capacity must also be maximized through the deployment of new techniques such as new multi-level modulation formats eventually combined with coherent detection, new techniques for multiplexing and routing (packet switching).

Optical Access and Code-Division Multiple Access

Wide adoption of optical access network requires upgrading existing PONs to share bandwidth among more users. An important access to research in PON architecture and components exists through collaboration and Cifre Thesis with FT-Orange labs on new generation PON including extended PON, WDM PON, etc.

On the other hand, we have developed a research activity on a more prospective access network possible evolution namely OCDMA multiplexing techniques. CDMA (Code Division Multiple Access) in the optical access networks allows scrambling and a flexible bandwidth resource sharing between users. For the direct optical detection channel, we have pointed out that the prime sequences (PS) appear as a good compromise between the length and the weight of the code and the number of users [464] Implementation of all-optical encoders and decoders using Bragg gratings recorded in photo-optical fibers (made in collaboration with the laboratory PhLAM of the University of Lille) has been analyzed for different code structures [466].

Additionally, the ANR-SUPERCODE⁶ project combines WDM and OCDMA by designing a supercontinuum pulsed source which can be shared among many users by being sliced into WDM channels [586, 597], each of which supports multiple users through all-optical encoding and decoding. This last work has started building on direct-sequence codes (DS-OCDMA), now with the extended quadratic congruence (EQC) code family for better multiuser performance.

However, this is still an amplitude-only coding scheme; as in the long-haul context, using the phase of the optical field would unlock the full bandwidth of the optical fiber, if it can be done in a cost-effective way for the access network. This is SUPERCODE's final objective: using spectral-phase coding thanks to enhanced FBG-based encoders using phase-shifted chirped Bragg gratings. This technique, which could either complement or even supersede WDM, should yield a much lower crosstalk between users on the same channel, thus supporting more users for a lower penalty.

Radio-Over-Fiber

The interface between the radio and optical fiber networks (radio-on-fiber), remote antennas for radar, introduce new challenges for fiber optic transmissions, which should preserve the dynamics and the linearity of the signal even in the presence of attenuation, GVD and PMD dispersion, non linearity transmission impairments and all optical signal processing. An original approach based on optical injection allows the heterodyne generation, transmission and remote control of radio frequency signals with high spectral quality [462, 635]. A generalization on injection locking optical phase synchronization has been proposed by using the Green function approach [482, 642, 580].

Metropolitan and Long-Haul Systems

Today's processing capability allows to perform digital signal processing for optical communication systems at high bit rates. In close collaboration with the Digital Communications group, the potential and future trends of electrical signal processing techniques to mitigate e.g. noise accumulation, linear and nonlinear distortions are beginning to be investigated.

Under the project ANR-ECOFAME⁷ we have modeled and simulated the physical channel of an optical ring WDM network architecture. We provide the parameters of the statistical distribution

⁶Partners: Institut Carnot de Bourgogne (leader), CNRS PhLAM, XLIM

⁷Partners: Alcatel, Orange, Prism Laboratories, XLIM

χ^2 and estimate the performance in collaboration with XLIM working on FEC implementation [528, 501]. An extension of the concept to mesh networks has been proposed.

On the other hand, the project ANR-TCHATER⁸ concerns the design of a real-time coherent receiver at 40Gbit/s using a FPGA implementation. Our contribution concerns the design of hard and soft FEC solutions adapted to optical coherent systems. Differential encoding is required for PSK transmission systems but leads to higher BER because each transmission error corrupts multiple consecutive bits. A new construction of the codeword has been proposed, allowing performance enhancement and complexity decrease [544, 645].

Finally, we have begun to explore higher spectral efficiency modulation formats, such as M-QAM and OFDM, that can allow enhanced bit-rate while reducing electronic circuits speed [551]. In collaboration with Orange labs, a comparison of 40Gb/s ultra-long haul WDM transmission system performances has been realized [493, 494].

Optical Packet Switching

The evolution of optical networks toward more efficient and more flexible architectures has led research work to wonder how the network could transmit and route optical packets or optical bursts directly on the optical layer. Studying the routing of optical packets requires an analysis of techniques allowing label recognition and transparent packet forwarding. On the basis of our knowledge of all-optical signal processing devices, we have developed several elements required for the design of a switching node, including an optical half-adder [502], a time-to-wavelength (series-to-parallel) converter [503] and an all-optical decoder [504]. Finally, in collaboration with partners teams of the FP6-ePhoton/One NoE, we have proposed an entire packet-switching node whose optical elements had all been demonstrated experimentally by one of the participating groups [505].

4.2.3 Quantum Communications and Cryptography

Faculty P. Gallion.

Main events Organisation of the CLEO Focus Meeting on “Nonlinear, Quantum and Chaotic Optics: New Directions in Photonics and Optical Communications” in ECOC’06 conference.

Projects ANR-HQNet (12/06–11/09).

Quantum key distribution (QKD) is the only known way to achieve cryptographic keys distribution with unconditional security. Quantum security first results from the impossibility to duplicate the signals received (non-cloning principle) or to take away a significant part of the signal without making the intervention known through a major change to the error rate of received signals. The security is based secondly on the disturbing or destroying character of any observation and the errors resulting from incompatible observations of a single quantum object.

Quantum cryptography is today leaving the promises of the physics of the last century for the trial implementations. The unconditional security range is obviously limited by optical channel and device impairments. Quantum cryptography must now prove its worth with the technological reality and deal with the algorithms and hardware securities, in a rich multi discipline problem including digital communications, optical communications, information theory, electronics signal processing and computing. In collaboration with our Electronics group and the Computer Science Department we have developed a validation platform for quantum cryptography gathering these various skills and involved in various projects such as the “High bit-rate and versatile Quantum Network” (ANR-HQNET^{footnote}Partners: FEMTO, Georgia Tech Lorraine, Smart Quantum, Photline) including also efforts from the FEMTO Laboratory of Besançon, the Georgia Tech Lorraine Laboratory and Smart Quantum and Photline companies.

⁸Partners: Alcatel-Lucent France (Leader), E2V semiconductors, INRIA Lyon, ENS Lyon

The compatibility of QKD with optical networks requires an operation at telecommunication wavelengths and does not allow polarization encoding. We proposed and validated the use of a QPSK phase modulation, turning to PSK signals after the Bob choice of basis, in association with a homodyne optical detection. As long as single-photon sources remain unavailable, we have to deal with non-orthogonal signals from quantum level coherent faint pulses. We conducted the theoretical and experimental comparison of a balanced homodyne receiver with a high local oscillator level using PIN photodiodes with an unconditionally hypothesis-canceling interferometric receiver on one of 2 photon counters [515]. Our homodyne receiver appears as a good alternative to the photon counters technique in view of its closeness to a quantum efficiency of 1, its thermal-effect-free operation and speed compatible with the key rate required by today applications. An intrinsic error rate resulting from the vacuum fluctuations entering through the signal port leads to a theoretical quantum bit error rate (QBER) which is approached quite easily in practice, the standard quantum limit (SQL). The use of a multiple-threshold decision allows for the optimization of the quality and the rate of the key generation. The mandatory recovery of both optical and data phases is provided by a time-multiplexed reference signal transmission and phase-tracking loop. We have shown the possibility of recovering the phase with performances very close to the quantum limit by sequential steps on both quadratures of the field received [514] and undertaken the study of security under different types of attack.

We have been among the first contributors to the apparition of quantum cryptography in IEEE optical communication journals and world largest conferences [526, 555, 557, 556, 487]. Finally, we have been invited by Emil Wolf to write a review chapter in the prestigious series "Progress in Optics" [640] and we have proposed a new general formulation for the Quantum macroscopic nonlinear optics [478].

4.3 References

4.3.1 ACL: Articles in ISI-Indexed Journals

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