

Quiz session

1. Would the 2-process Peterson's algorithm work if we reverse the order of the first two lines the 2-process Peterson's algorithm (first set turn and then flag)
2. Classify the following properties into safety/liveness. Justify your answers.
 - Every process eventually decides.
 - No two processes decide differently.
 - Every process eventually returns a previously proposed value or crashes (stops taking steps).
 - No two correct processes decide differently.
3. Consider the implementation of an M-valued one-writer N-reader (1WNR) regular register (Transformation III in the slides).
 - In the code of `write(v)`, is it possible to change the order of operations: first write 0 to $R[v-1], \dots, R[1]$ and then write 1 to $R[v]$?
 - What if the writer writes 0 to $R[1], \dots, R[v - 1]$ in the ascending order?

Justify your answers (e.g., by presenting an execution that violates the properties of a regular register).